
Reference device marking schematics for STM32 microcontrollers and microprocessors

Introduction

This document provides the most probable ⁽¹⁾ device marking example for every package code ⁽²⁾ encountered in STM32 general-purpose microcontrollers or microprocessors.

The package code is a unique package identifier that can be found:

- in this document, between parenthesis, at the end of each section title
- in this document, as a fragment of the index located at each figure bottom-right corner
- in the product datasheet, between parenthesis, at the end of each section title, or as a fragment of the index located at the bottom-right corner of package outline figures.

(1): Some slight package marking variations may be encountered between different products using the same package, and even between two identical products (in case of multiple assembly plants for instance). Nevertheless, the pin 1 / ball A1 localisation instructions provided in this document apply invariably.

(2): Except wafer level chip scale packages (WLCSP) that remain described in the product datasheet.



1 General information

This document applies to STM32 Arm®-based general-purpose microcontrollers or microprocessors.

Note: Arm is a registered trademark of Arm Limited (or its subsidiaries) in the US and/or elsewhere.

arm

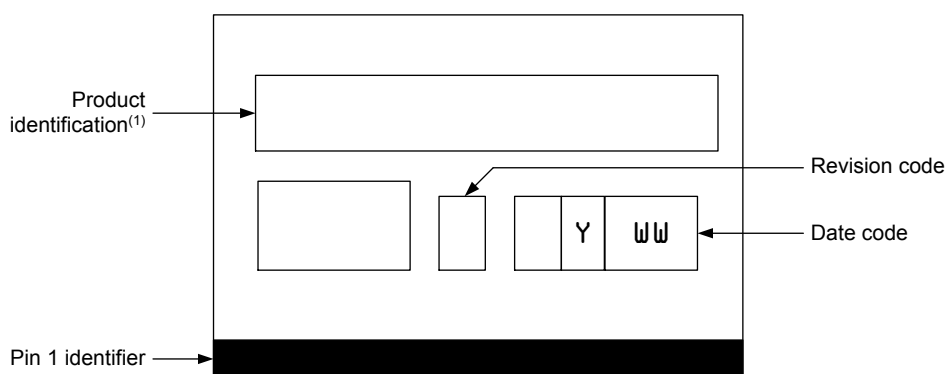
2 Device markings for SO packages

2.1 Device marking for SO8N 4.9 × 6 mm (O7)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 1. SO8N marking example (package top view)



O7_SO8N_DM_V5

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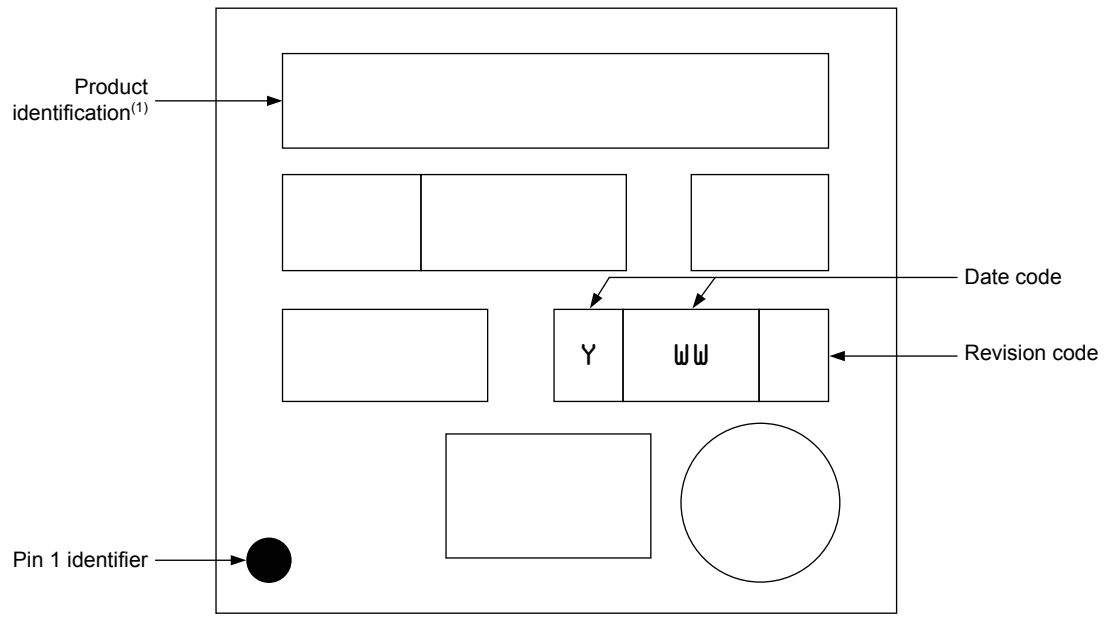
3 Device markings for TSSOP packages

3.1 Device marking for TSSOP14 5 × 4.4 mm (6R)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 2. TSSOP14 marking example (package top view)



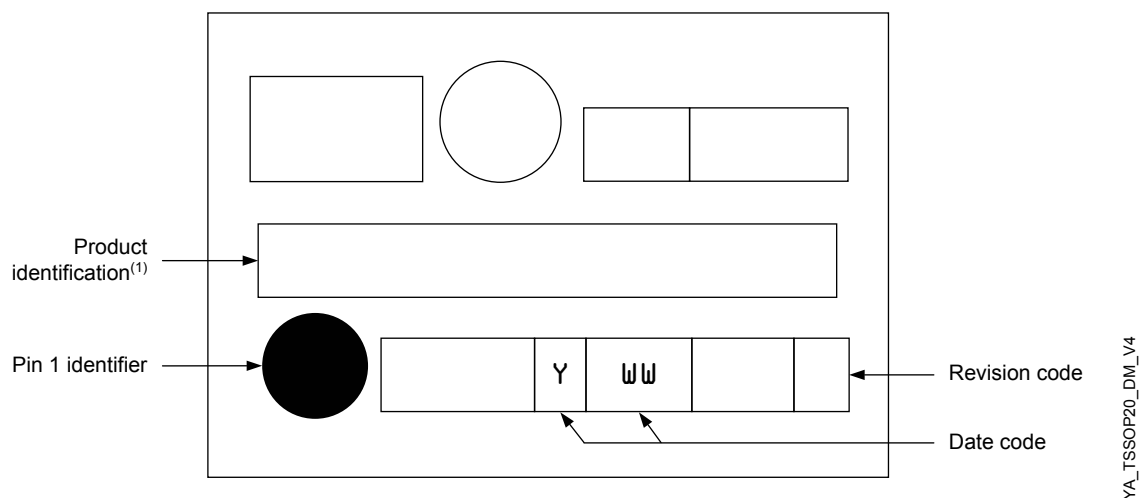
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3.2 Device marking for TSSOP20 6.5 × 4.4 mm (YA)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 3. TSSOP20 marking example (package top view)



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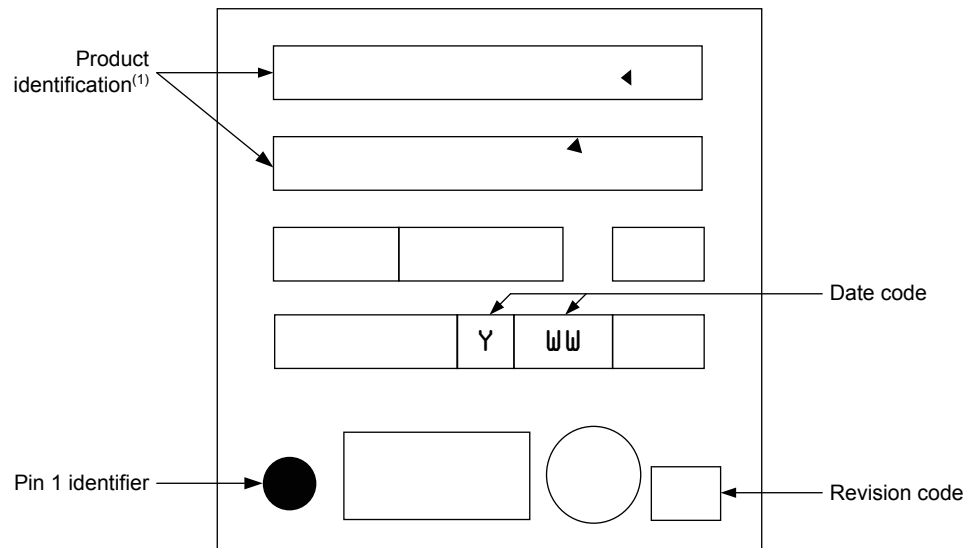
4 Device markings for LQFP packages

4.1 Device marking for LQFP32 7 × 7 mm (5V)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 4. LQFP32 marking example (package top view)



5V_LQFP32_DM_V2

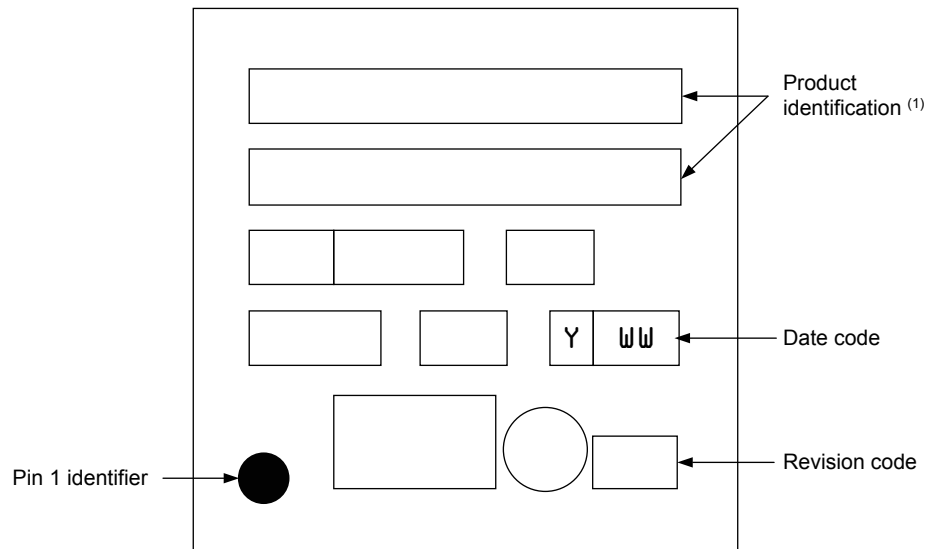
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4.2 Device marking for LQFP44 10 × 10 mm (4Y)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 5. LQFP44 marking example (package top view)



4Y_LQFP44_DM_V2

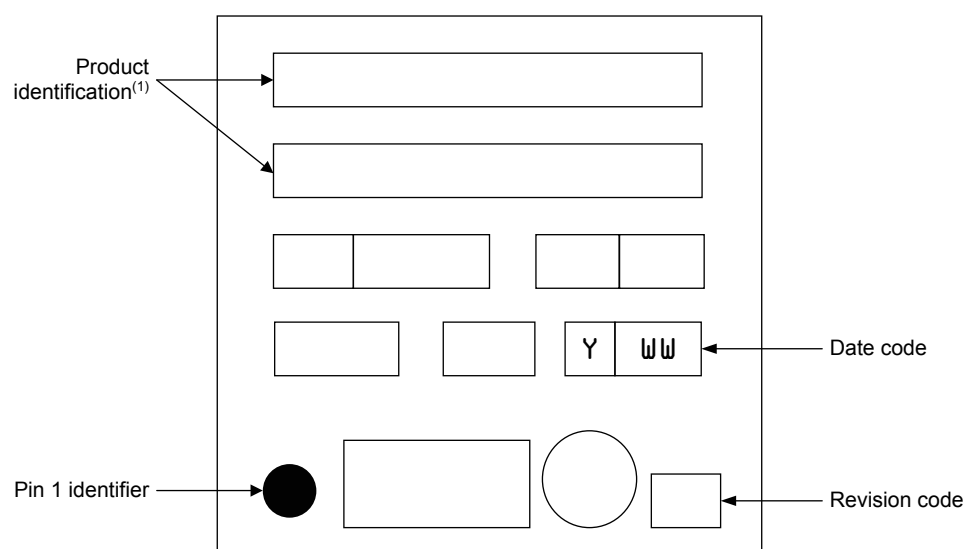
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4.3 Device marking for LQFP48 7 × 7 mm (5B)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 6. LQFP48 marking example (package top view)



5B_LQFP48_DM_V2

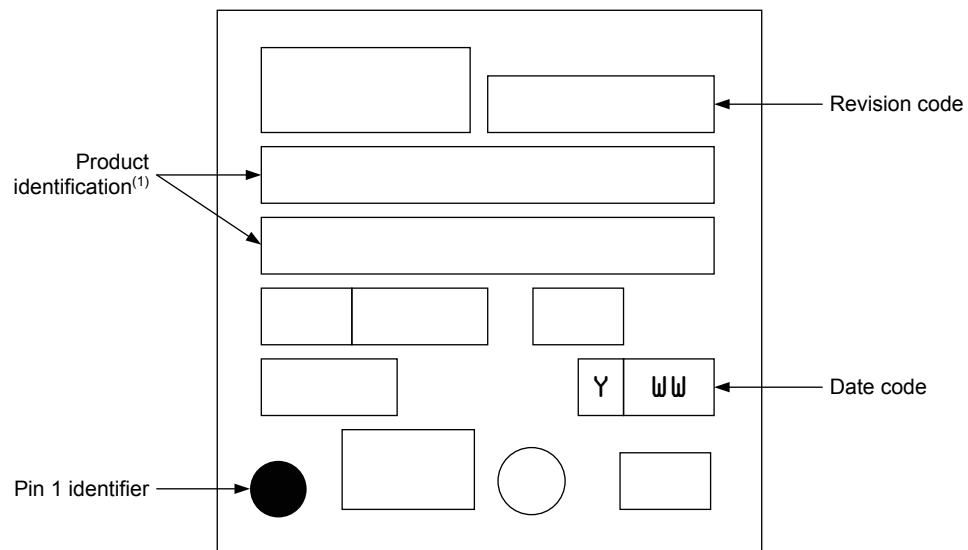
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4.4 Device marking for LQFP64 10 × 10 mm (5W)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 7. LQFP64 marking example (package top view)



5W_LQFP64_DM_V3

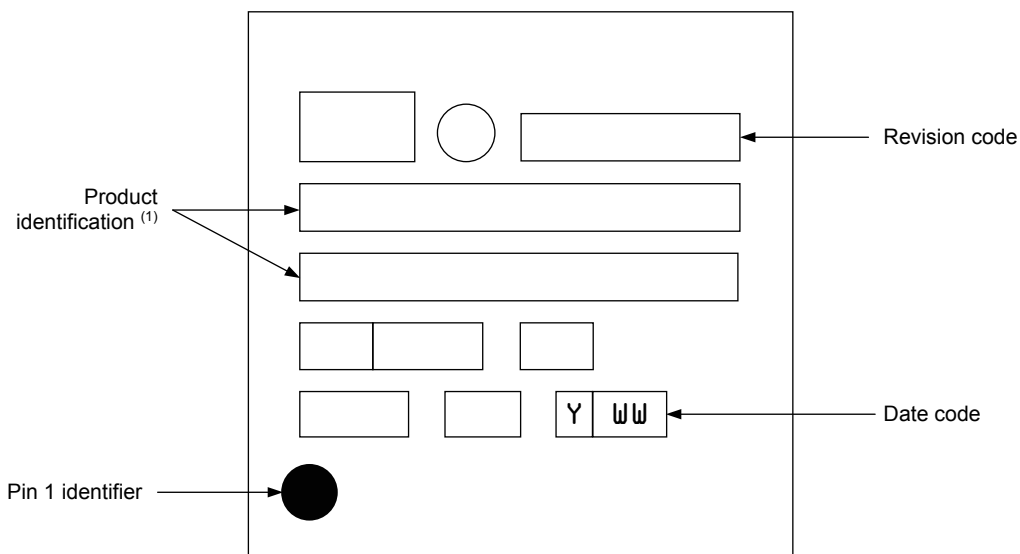
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4.5 Device marking for LQFP64 14 × 14 mm (1R)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 8. LQFP64 marking example (package top view)



1R_LQFP64_DM_V2

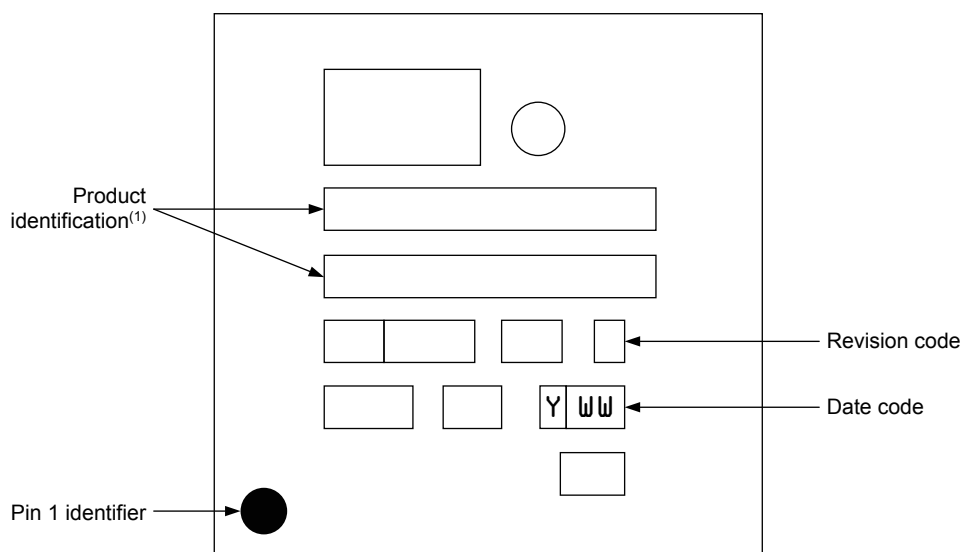
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4.6 Device marking for LQFP80 12 × 12 mm (9X)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 9. LQFP80 marking example (package top view)



9X_LQFP80_DM_V2

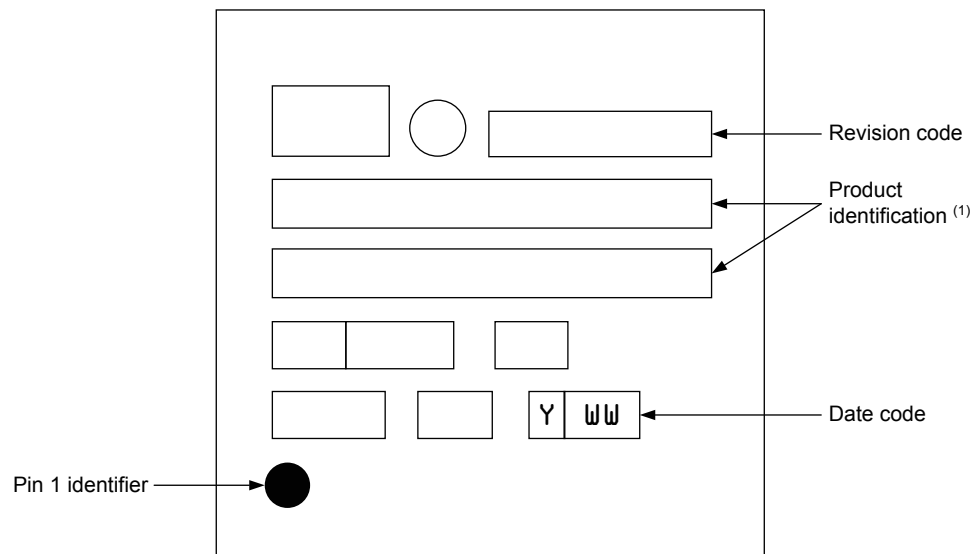
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4.7 Device marking for LQFP80 14 × 14 mm (1S)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 10. LQFP80 marking example (package top view)



1S_LQFP80_DM_V2

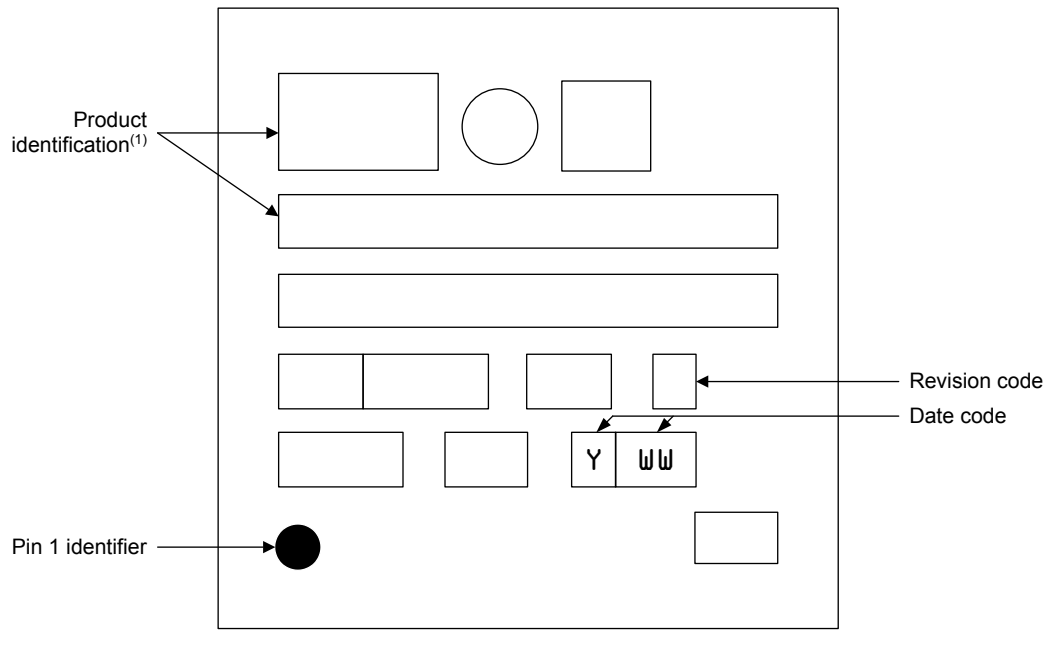
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4.8 Device marking for LQFP100 14 × 14 mm (1L)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 11. LQFP100 marking example (package top view)



1L_LQFP100_DM_V2

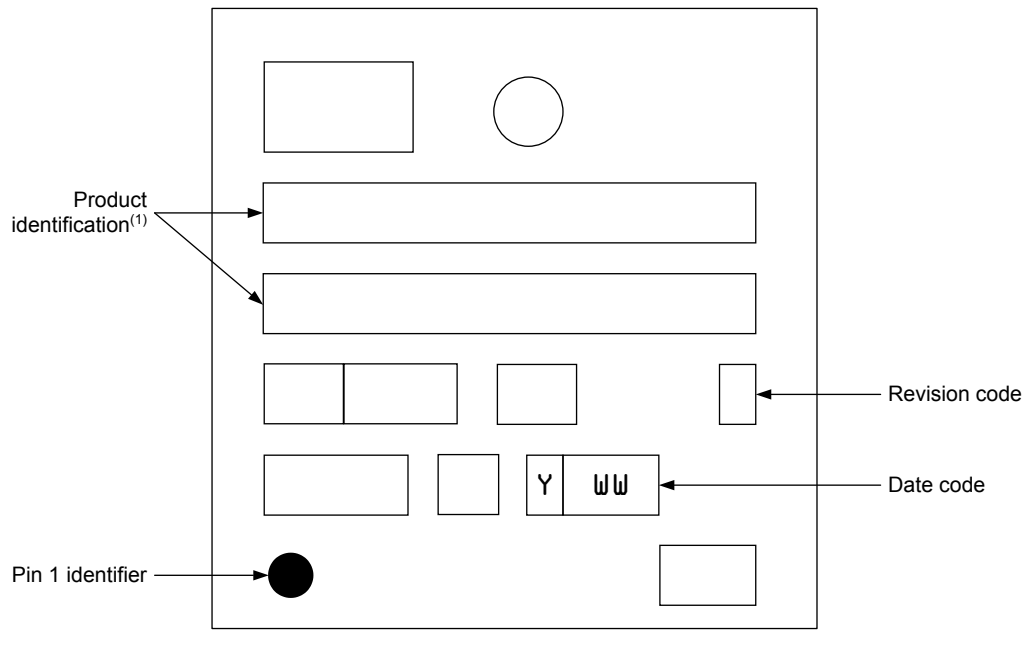
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4.9 Device marking for LQFP128 14 × 14 mm (TC)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 12. LQFP128 marking example (package top view)



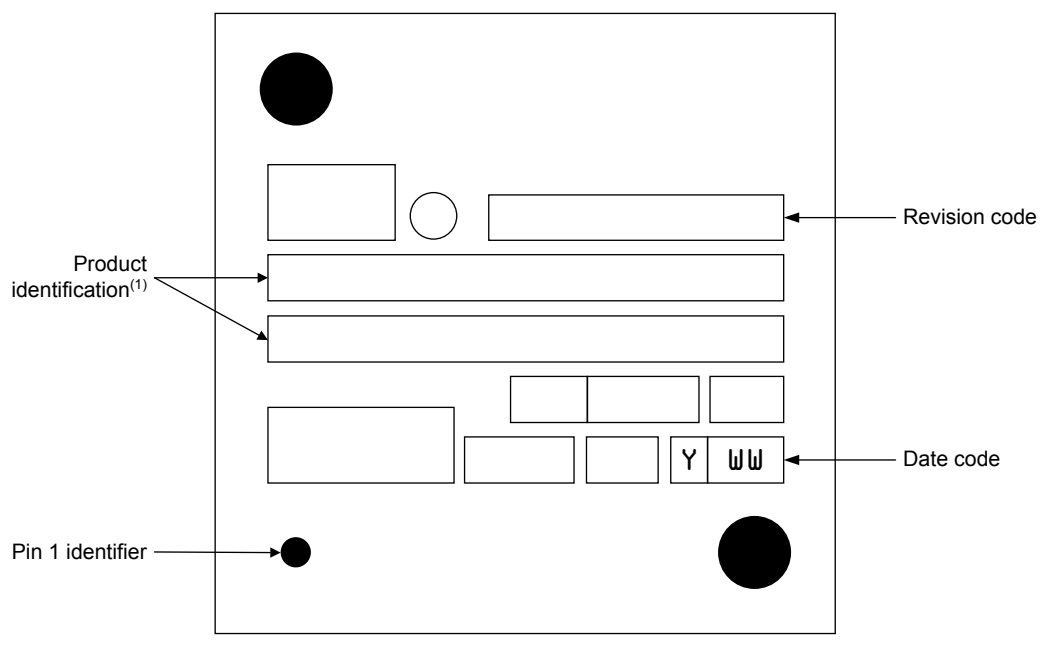
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4.10 Device marking for LQFP144 20 × 20 mm (1A)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 13. LQFP144 marking example (package top view)



1A_LQFP144_DM_V1

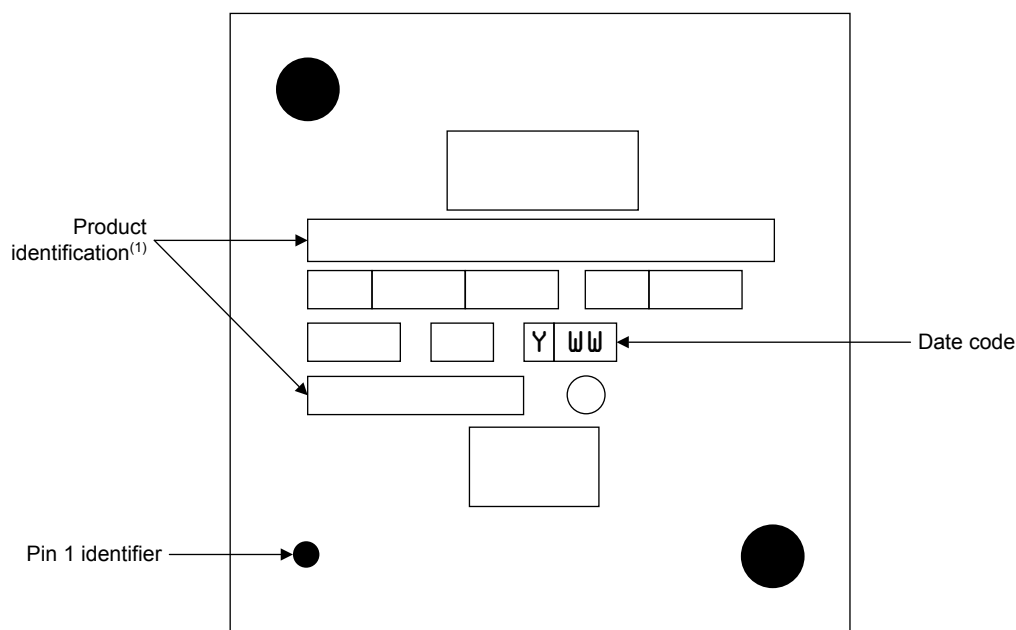
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4.11 Device marking for LQFP176 24 × 24 mm (1T)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 14. LQFP176 marking example (package top view)



1T_LQFP176_DM_V1

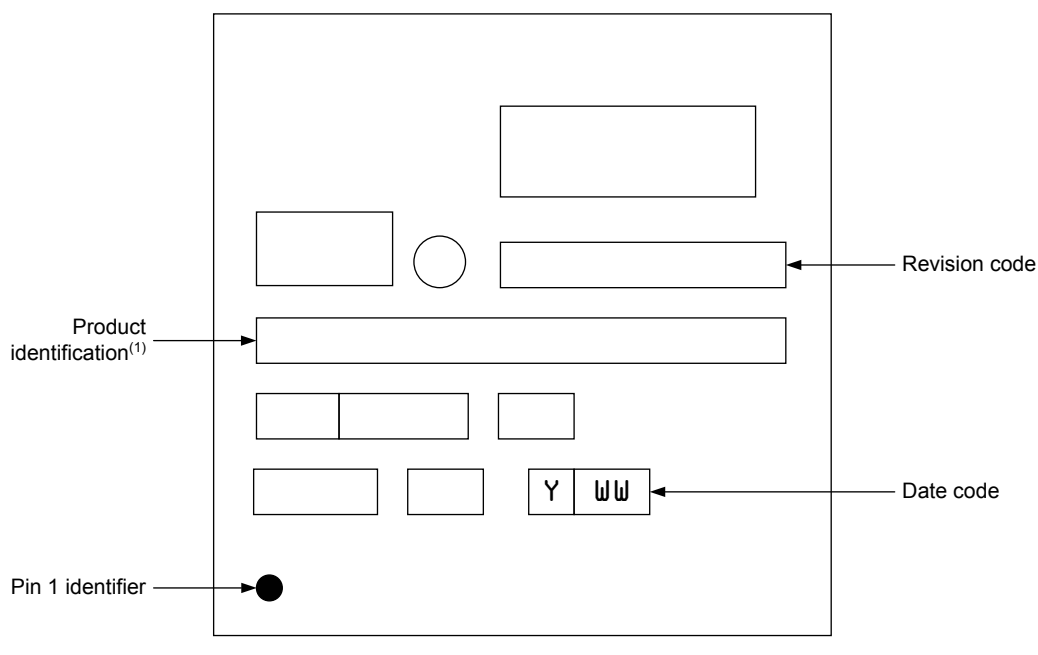
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4.12 Device marking for LQFP208 28 × 28 mm (UH)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 15. LQFP208 marking example (package top view)



UH_LQFP208_DM_V2

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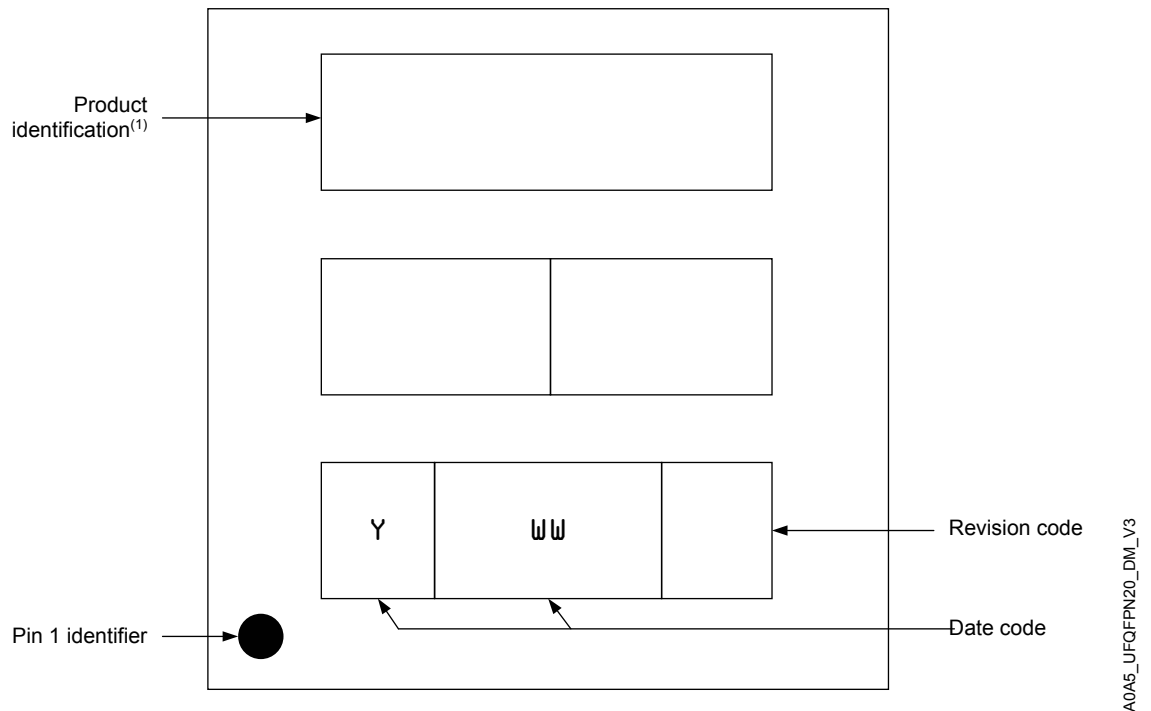
5 Device markings for UFQFPN packages

5.1 Device marking for UFQFPN20 3 × 3 mm (A0A5)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 16. UFQFPN20 marking example (package top view)



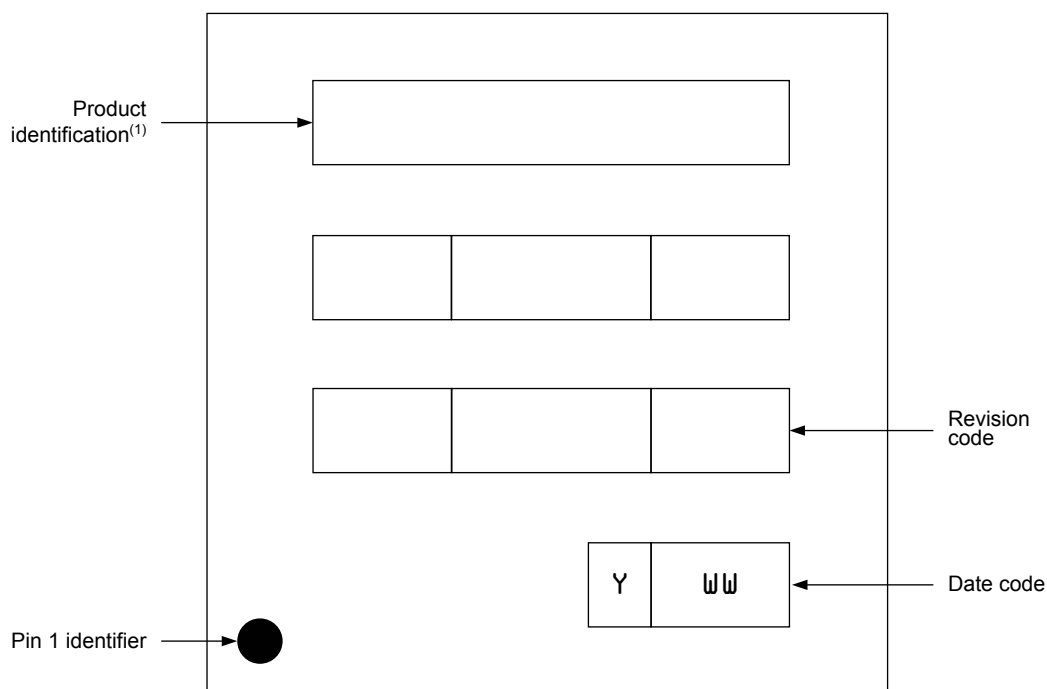
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5.2 Device marking for UFQFPN28 4 × 4 mm (A0B0)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 17. UFQFPN28 marking example (package top view)



A0B0_UFQFPN28_DM_V4

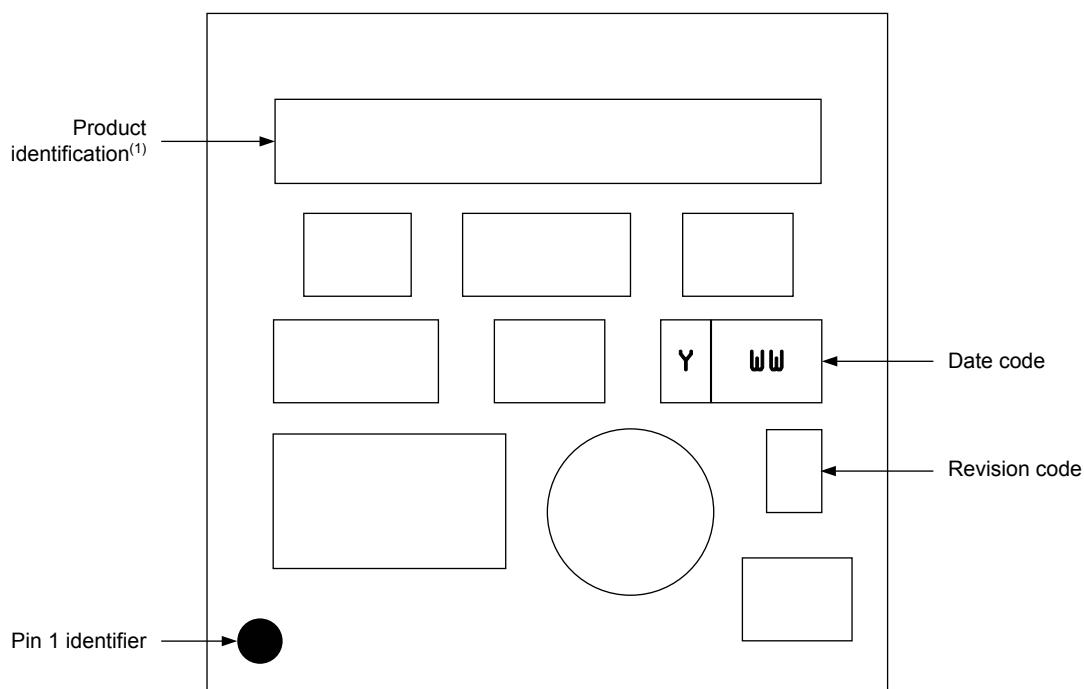
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

5.3 Device marking for UFQFPN32 5 × 5 mm (A09E)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 18. UFQFPN32 marking example (package top view)



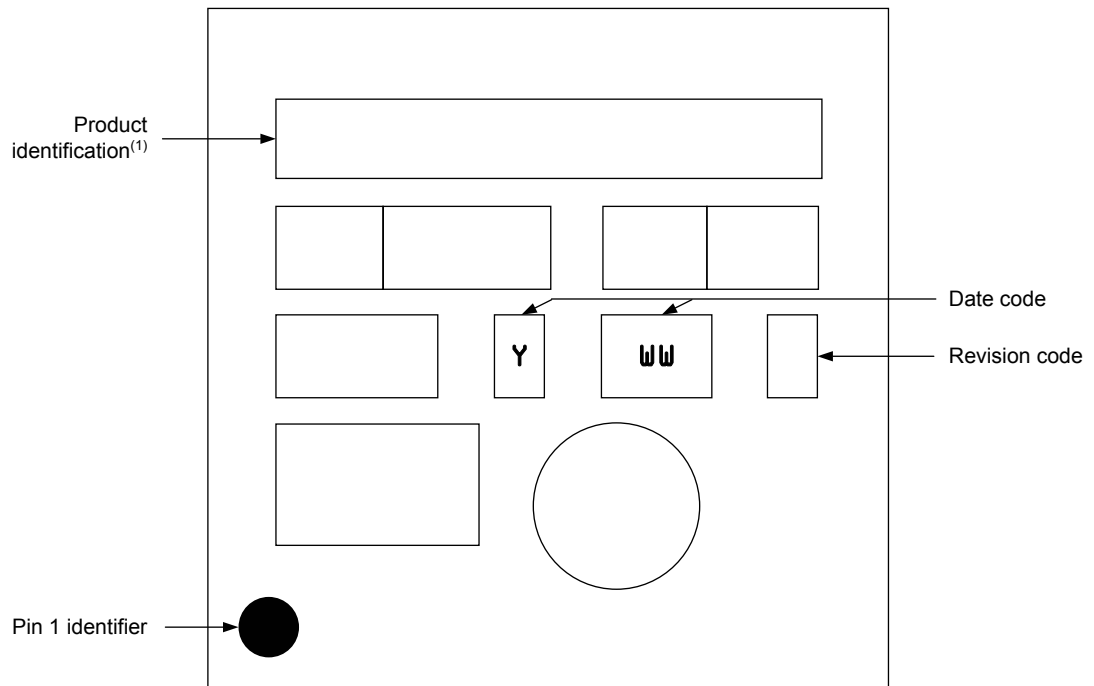
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5.4 Device marking for UFQFPN32 5 × 5 mm (A0B8)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 19. UFQFPN32 marking example (package top view)



A0B8_UFQFPN32_DM_V2

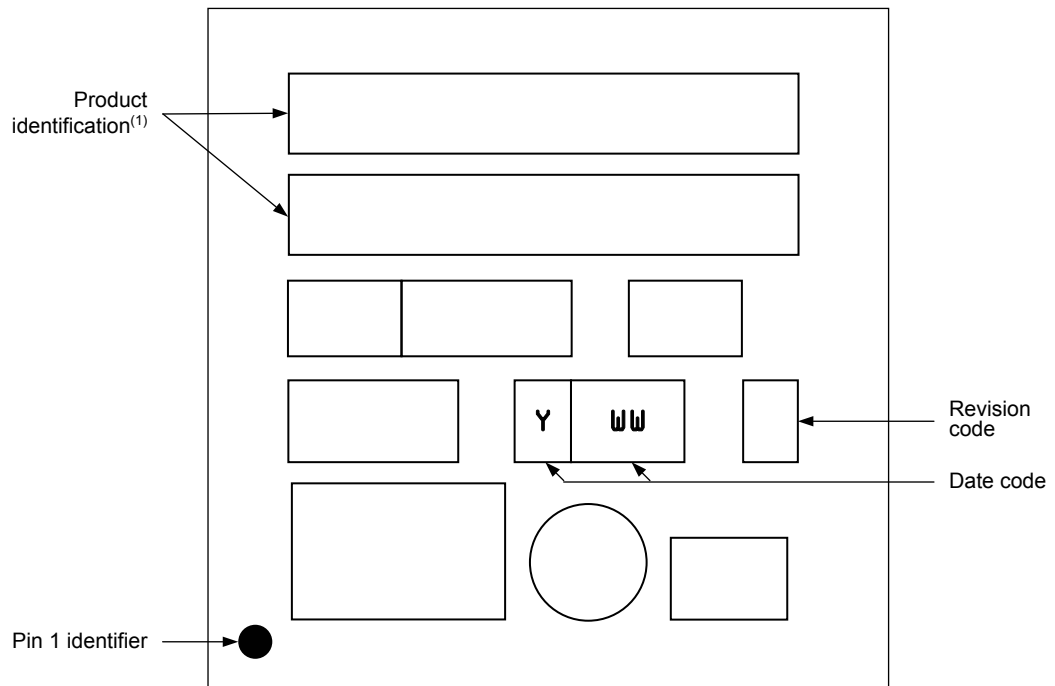
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5.5 Device marking for UFQFPN48 6 × 6 mm (A0F2)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 20. UFQFPN48 marking example (package top view)



A0F2_UFQFPN48_DM_V1

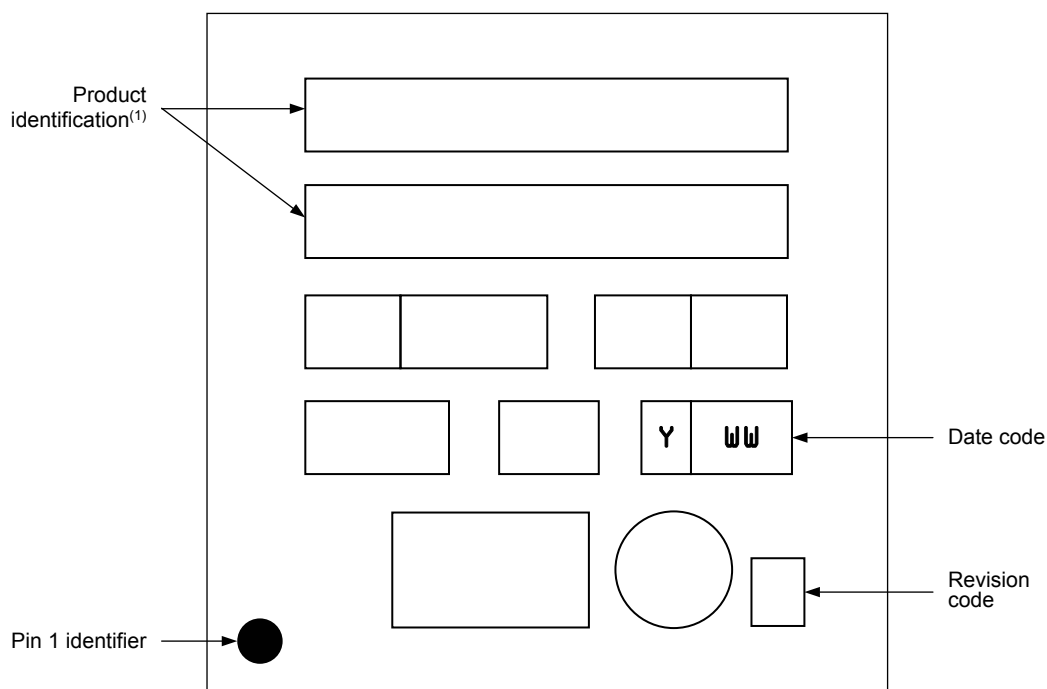
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5.6 Device marking for UFQFPN48 7 × 7 mm (A0B9)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 21. UFQFPN48 marking example (package top view)



A0B9_UFQFPN48_DM_V4

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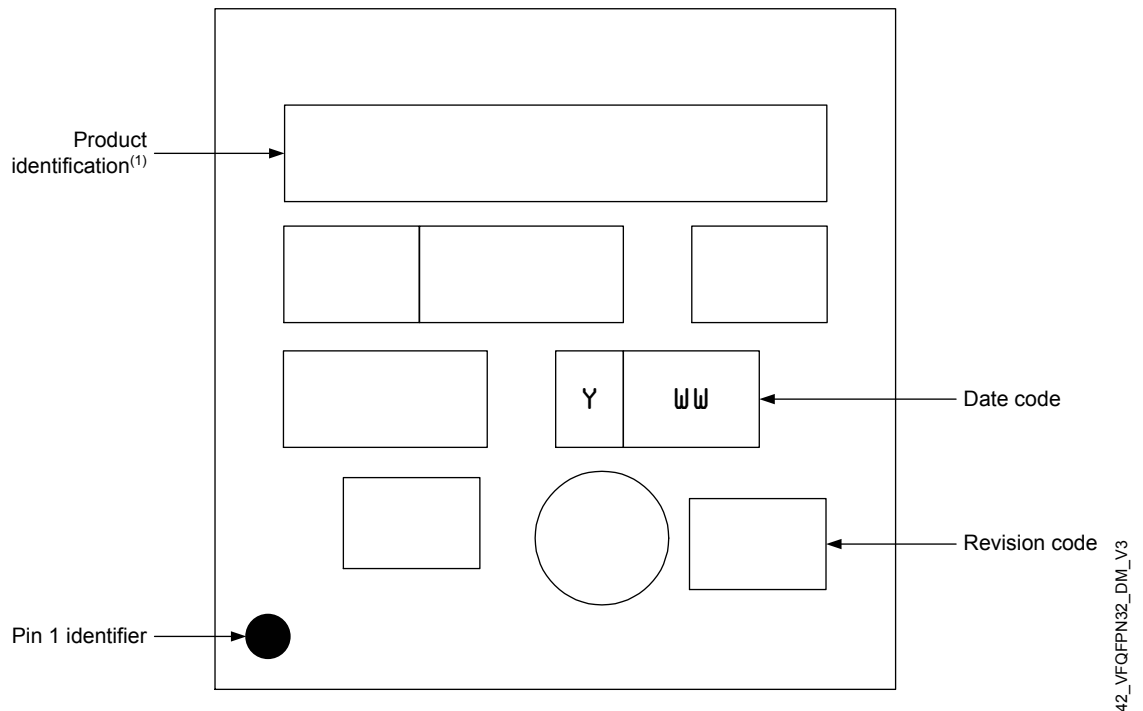
6 Device markings for VFQFPN packages

6.1 Device marking for VFQFPN32 5 × 5 mm (42)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 22. VFQFPN32 marking example (package top view)



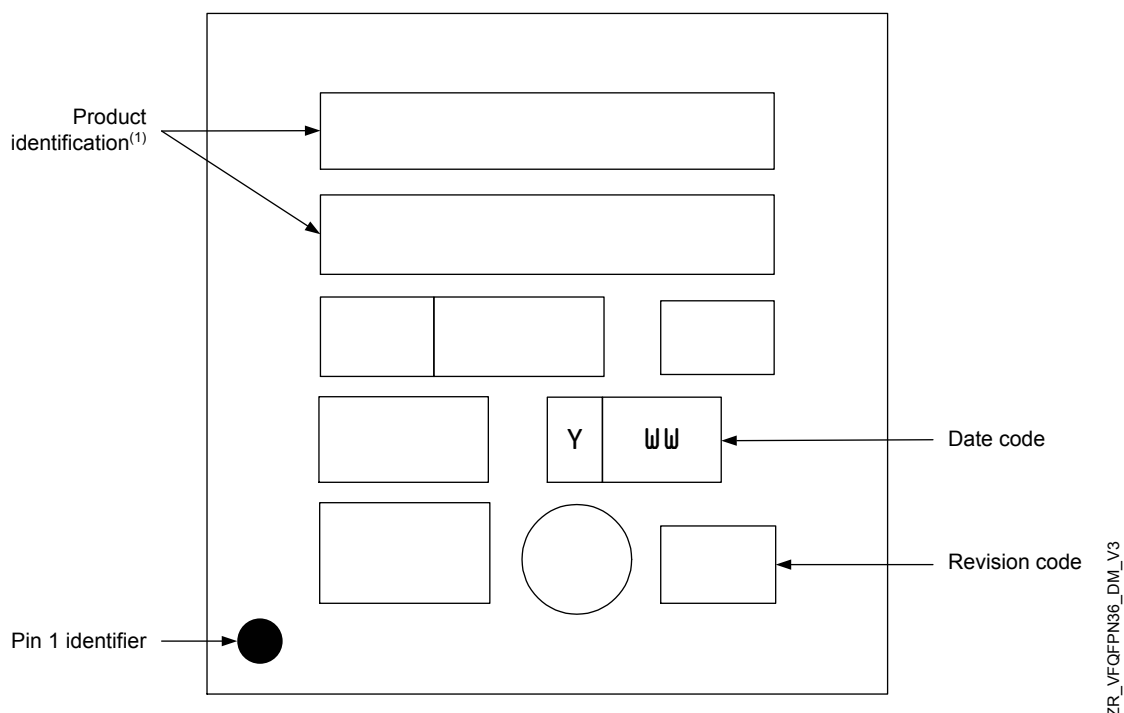
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6.2 Device marking for VFQFPN36 6 × 6 mm (ZR)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 23. VFQFPN36 marking example (package top view)



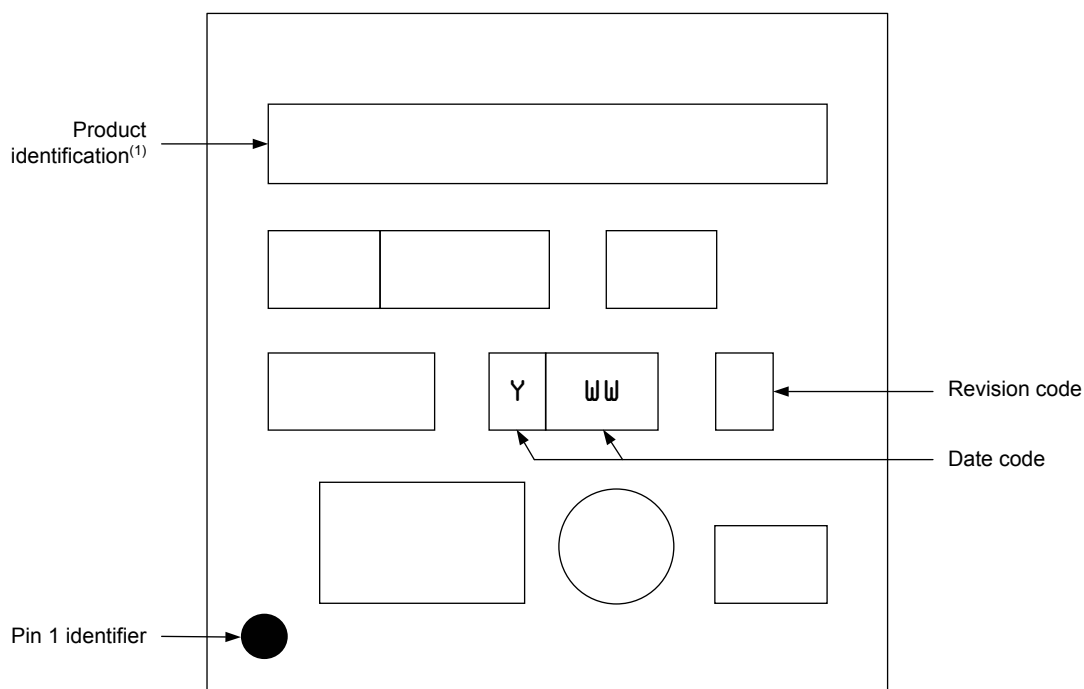
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6.3 Device marking for VFQFPN48 6 × 6 mm (A0BE)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 24. VFQFPN48 marking example (package top view)



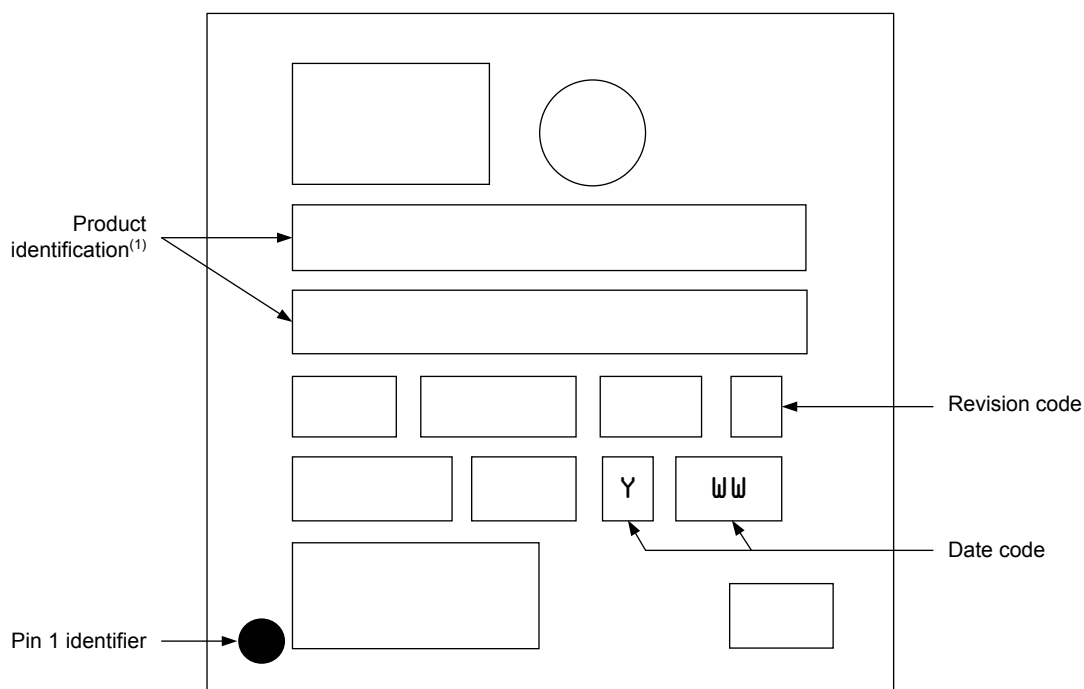
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6.4 Device marking for VFQFPN68 8 × 8 mm (B029)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 25. VFQFPN68 marking example (package top view)



3029_VFQFPN68_DM_V6

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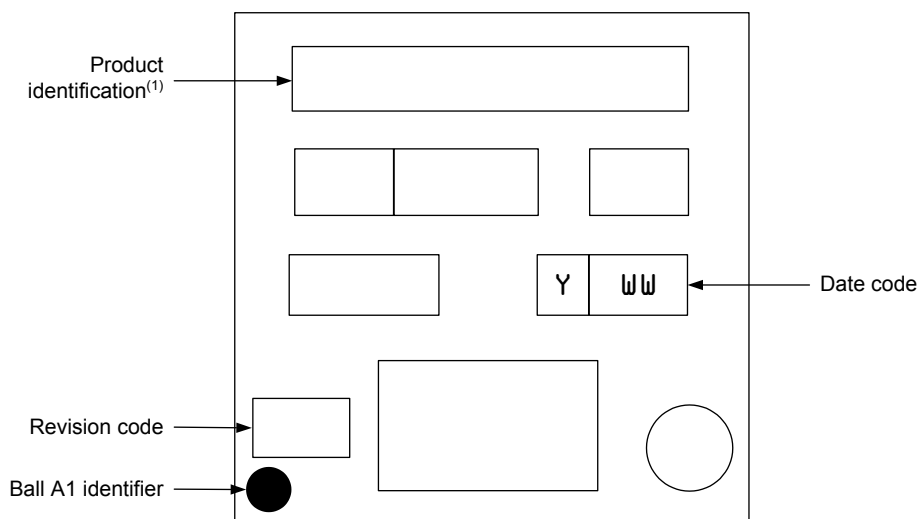
7 Device markings for LFBGA packages

7.1 Device marking for LFBGA36 6 × 6 mm (AL)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 26. LFBGA36 marking example (package top view)



AL_LFBGA36_DM_V2

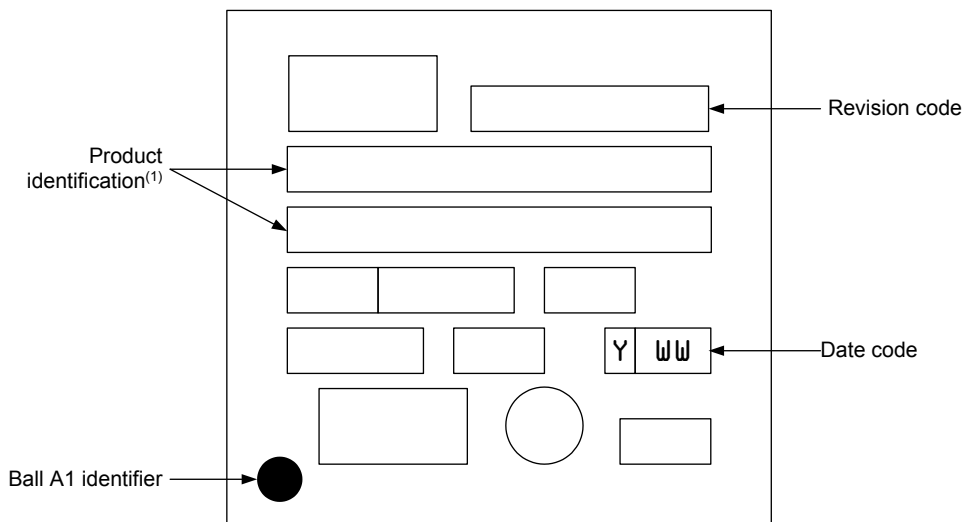
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST’s Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

7.2 Device marking for LFBGA100 10 × 10 mm (H0)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 27. LFBGA100 marking example (package top view)



H0_LFBGA100_DM_V3

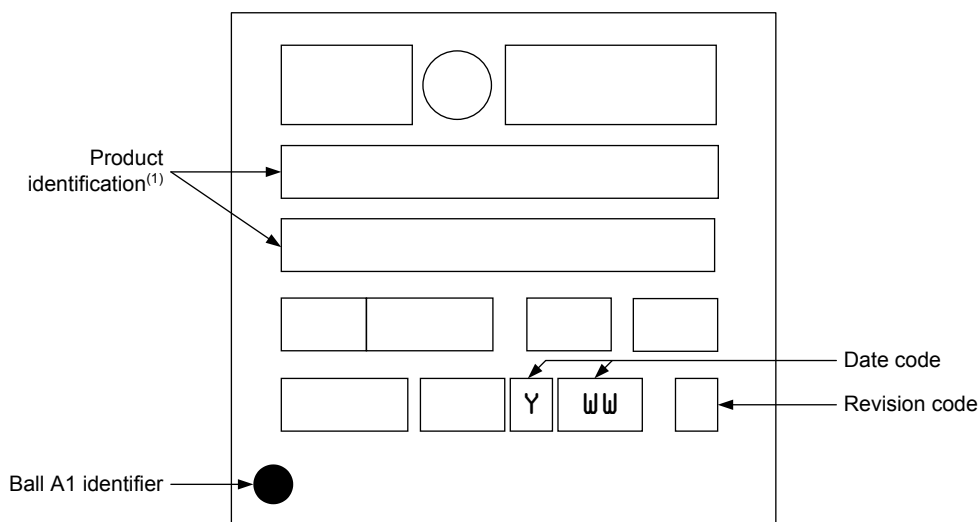
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

7.3 Device marking for LFBGA144 10 × 10 mm (X3)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 28. LFBGA144 marking example (package top view)



X3_LFBGA144_DM_V3

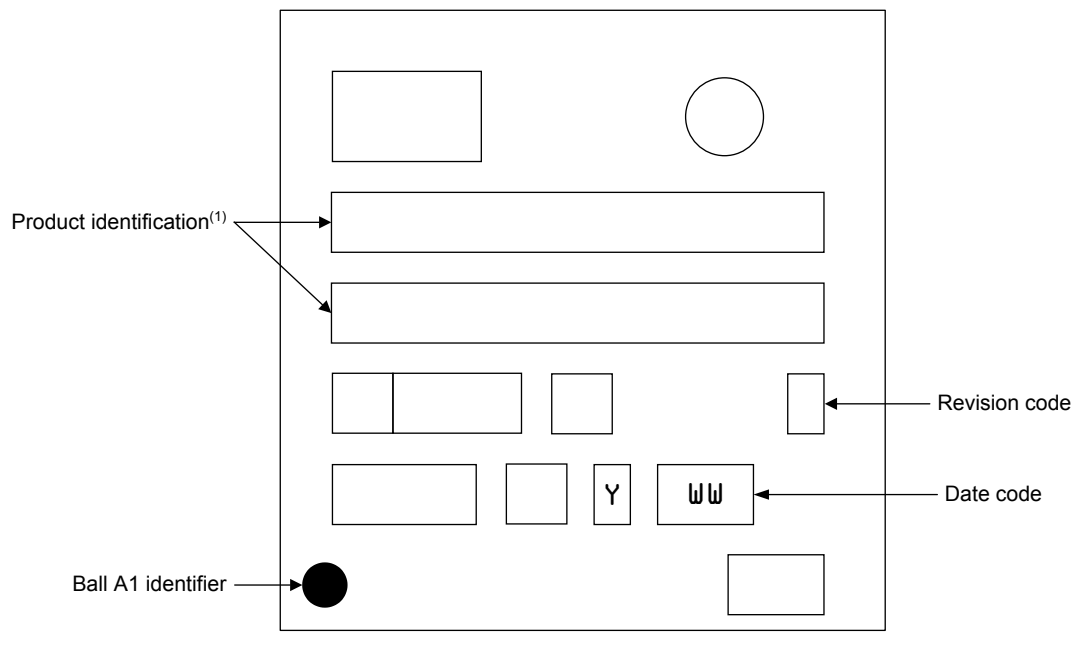
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST’s Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

7.4 Device marking for LFBGA289 14 × 14 mm (B0ED)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 29. LFBGA289 marking example (package top view)



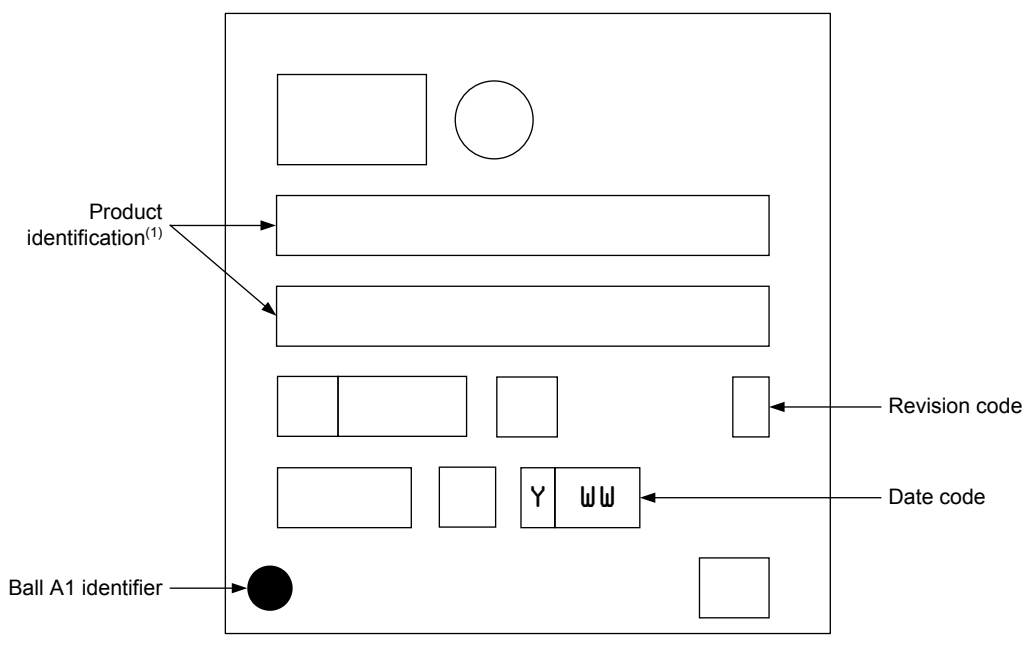
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST’s Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

7.5 Device marking for LFBGA354 16 × 16 mm (B02Z)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 30. LFBGA354 marking example (package top view)



B02Z_LFBGA354_DM_V2

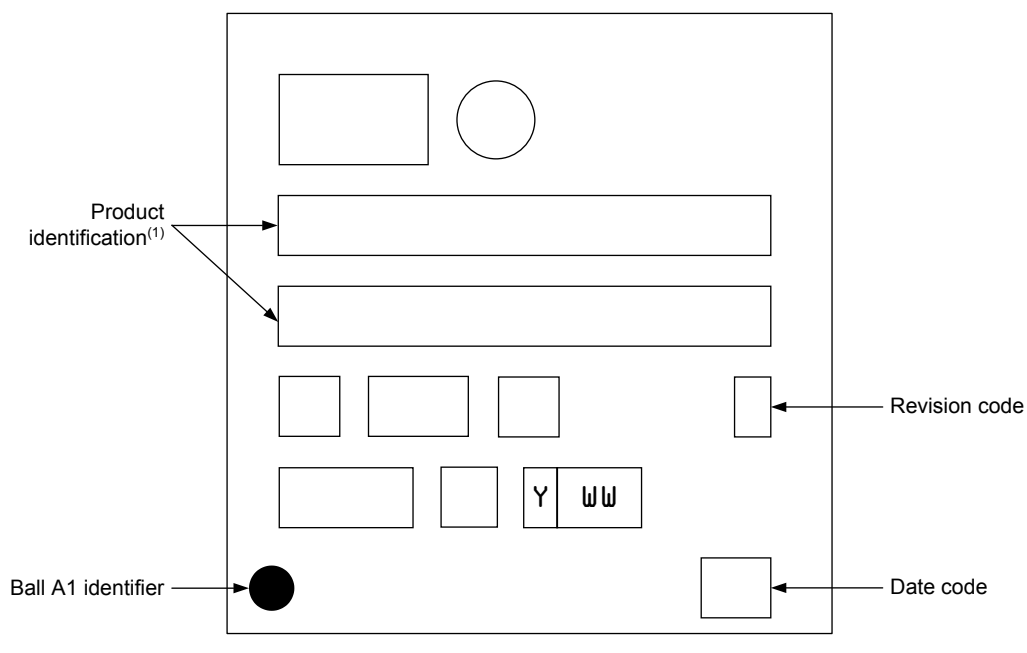
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST’s Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

7.6 Device marking for LFBGA448 18 × 18 mm (B032)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 31. LFBGA448 marking example (package top view)



B032_LFBGA448_DM_V2

1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

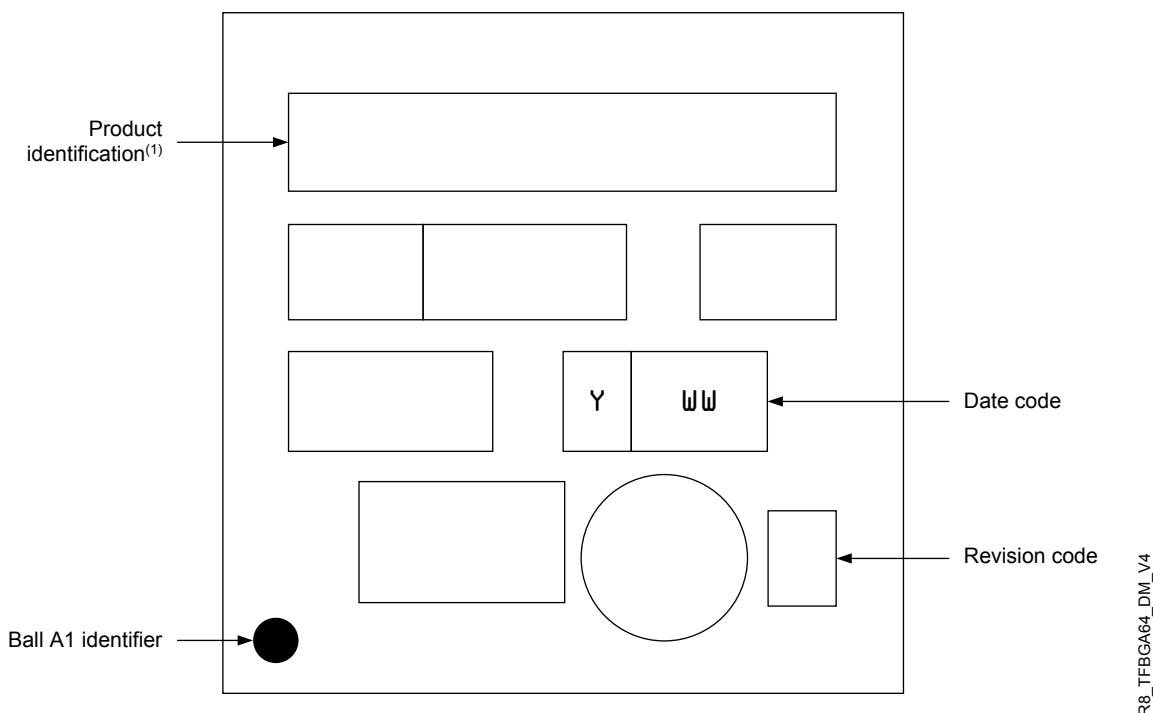
8 Device markings for TFBGA packages

8.1 Device marking for TFBGA64 5 × 5 mm (R8)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 32. TFBGA64 marking example (package top view)



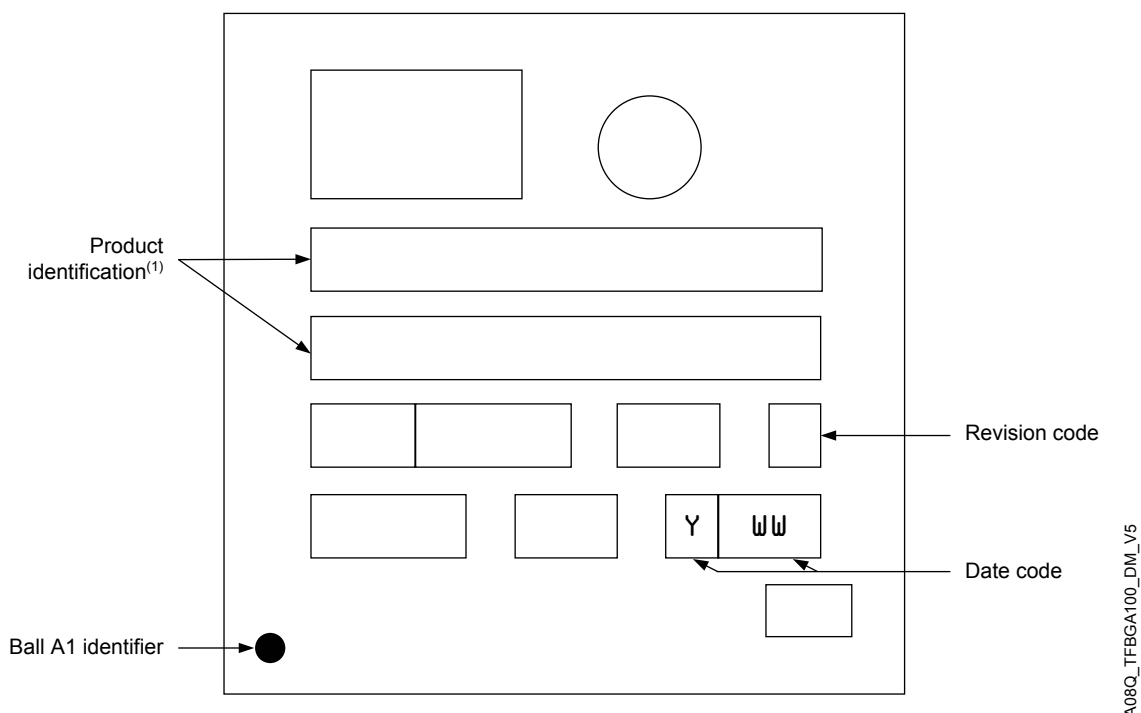
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST’s Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

8.2 Device marking for TFBGA100 8 × 8 mm (A08Q)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 33. TFBGA100 marking example (package top view)



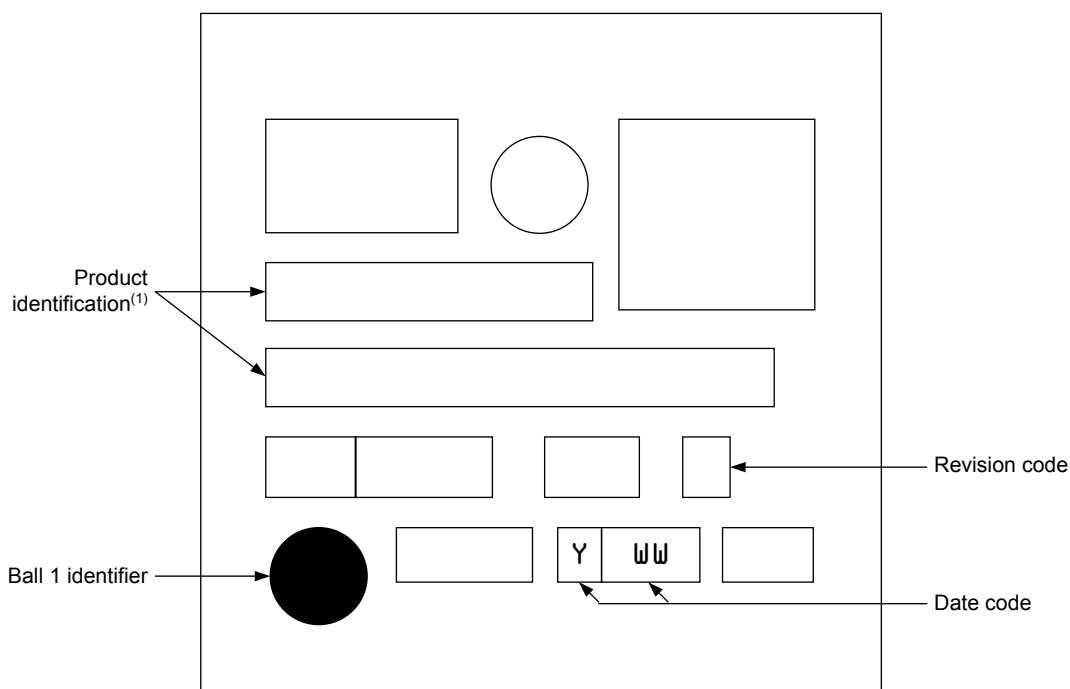
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

8.3 Device marking for TFBGA169 7 × 7 mm (B0MA)

The following figure gives an example of the locations and orientation of the marking areas versus ball 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball 1 is always located at bottom left.

Figure 34. TFBGA169 marking example (package top view)



B0MA_TFBGA169_DM_V1

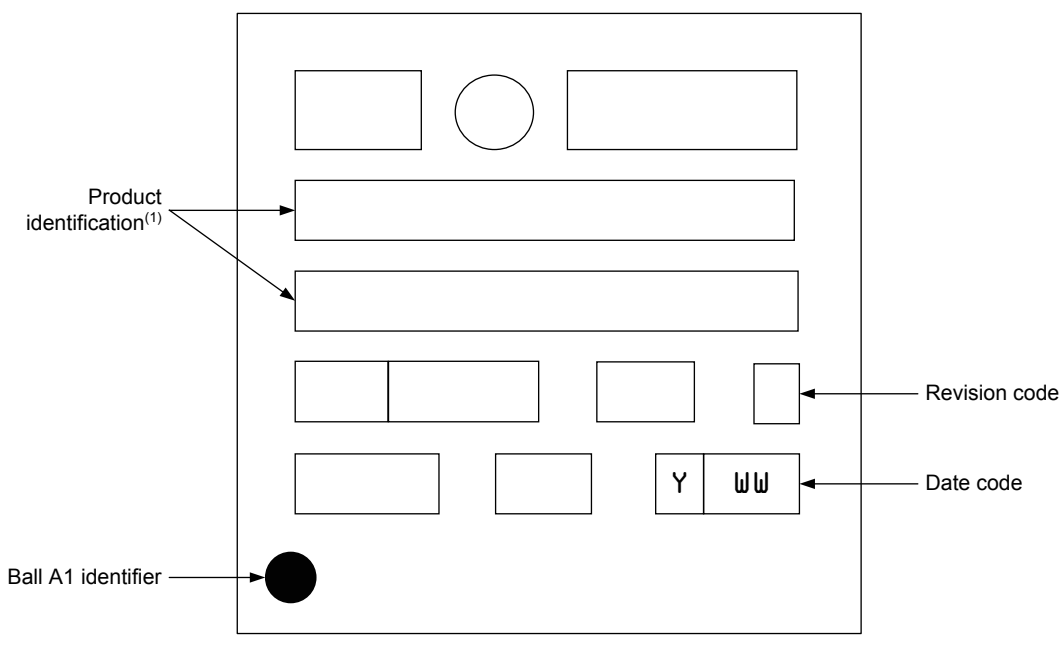
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

8.4 Device marking for TFBGA216 13 × 13 mm (A0L2)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 35. TFBGA216 marking example (package top view)



A0L2_TFBGA216_DM_V2

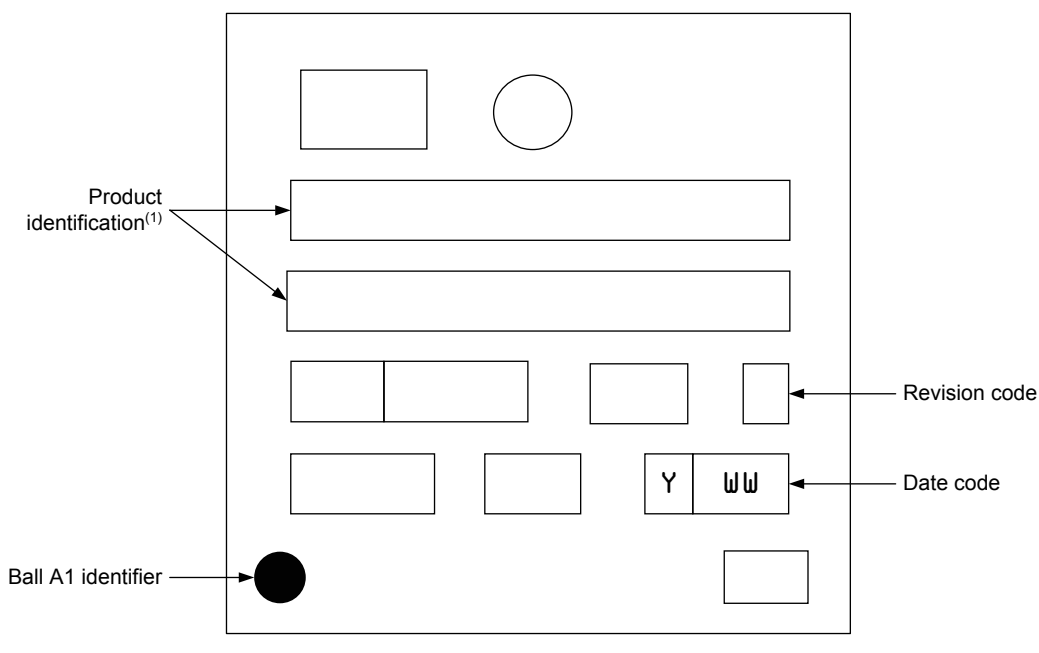
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST’s Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

8.5 Device marking for TFBGA225 13 × 13 mm (B04V)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 36. TFBGA225 marking example (package top view)



B04V_TFBGA225_DM_V2

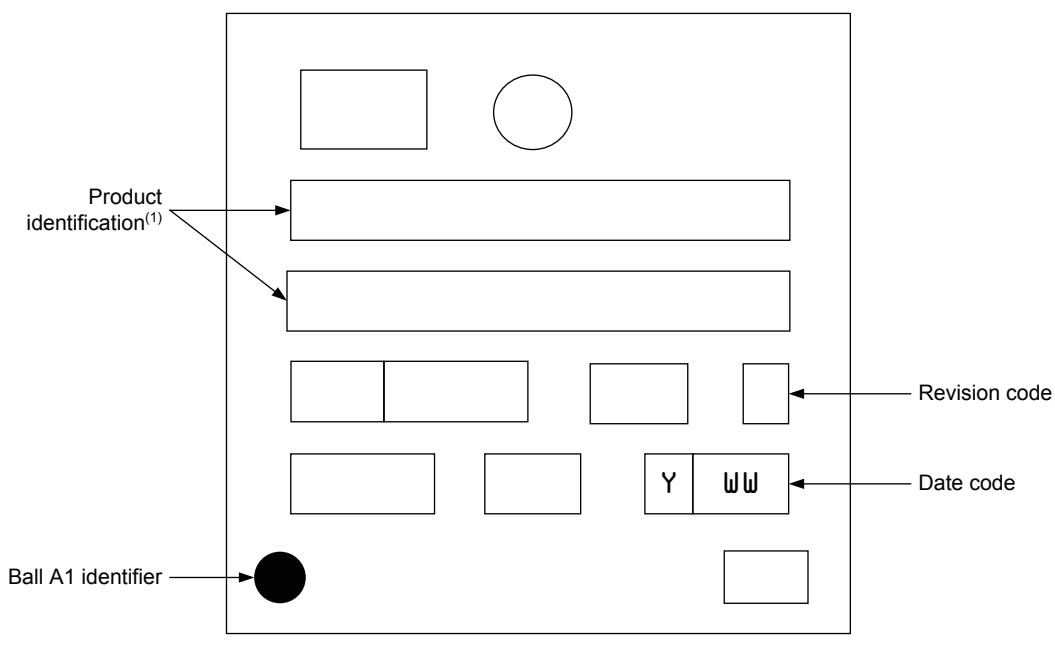
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST’s Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

8.6 Device marking for TFBGA257 10 × 10 mm (B02Y)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 37. TFBGA257 marking example (package top view)



302Y_TFBGA257_DM_V2

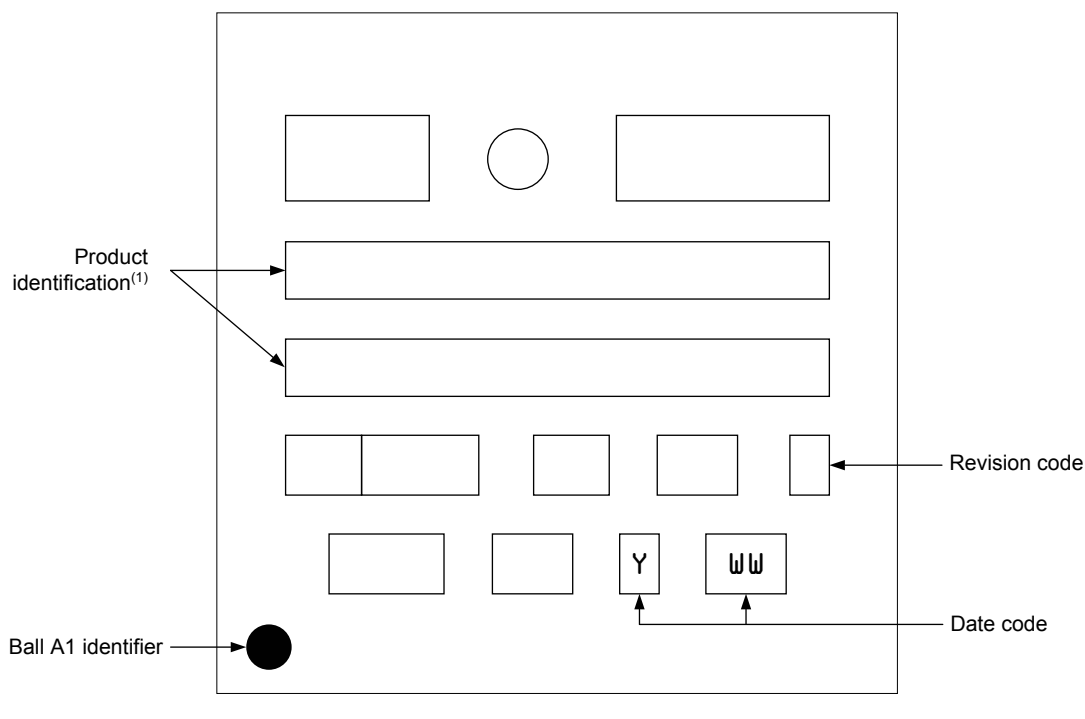
1. *Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST’s Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.*

8.7 Device marking for TFBGA240+25 14 × 14 mm (A07U)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 38. TFBGA240+25 marking example (package top view)



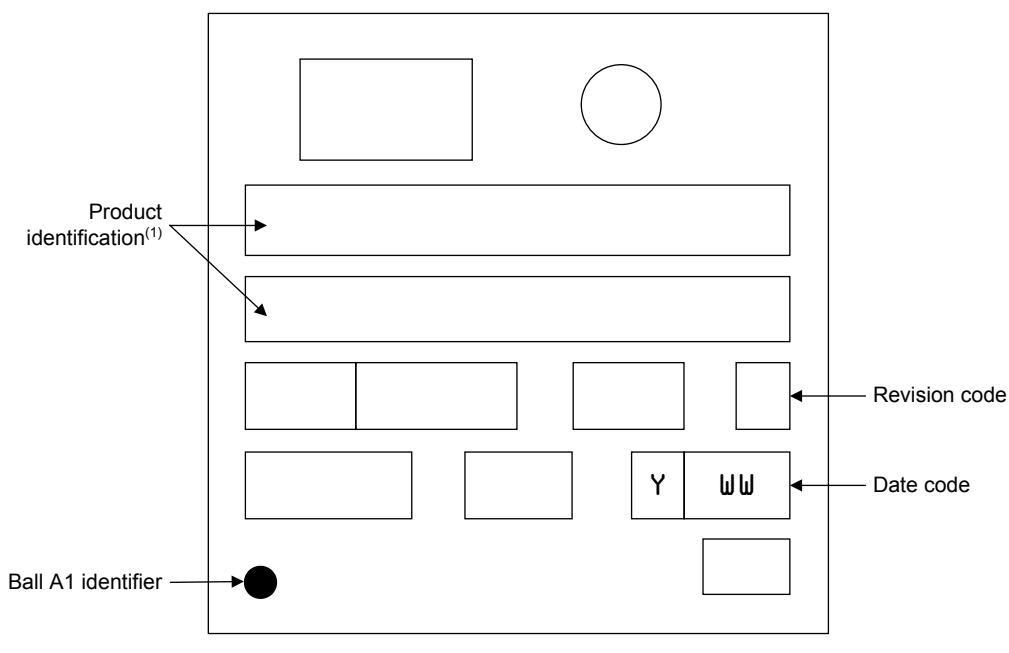
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

8.8 Device marking for TFBGA289 9 × 9 mm (B0EB)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 39. TFBGA289 marking example (package top view)



B0EB_TFBGA289_DM_V1

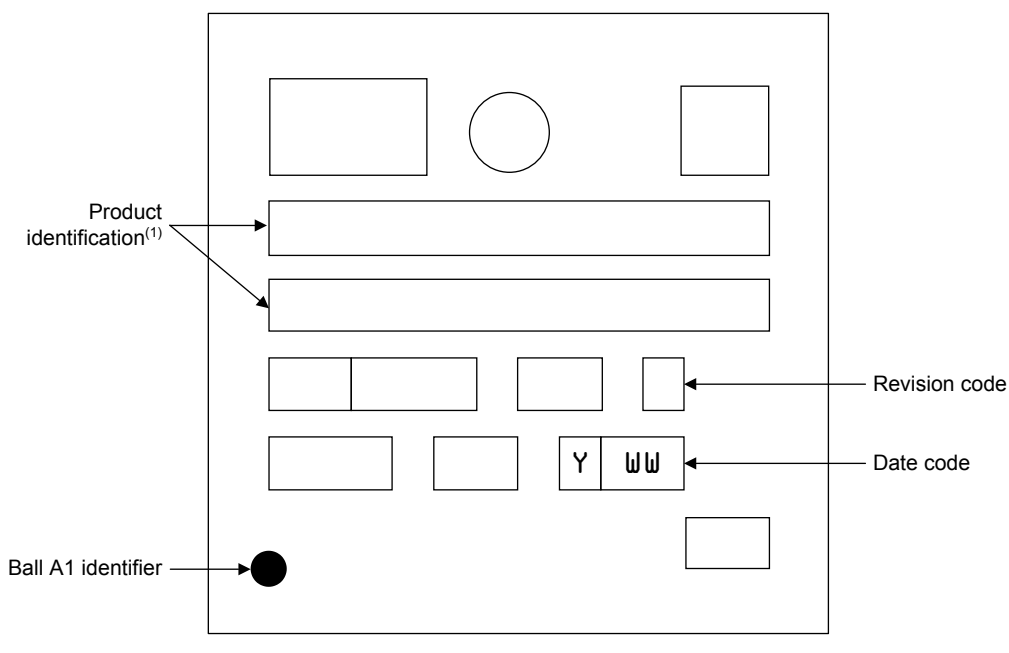
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST’s Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

8.9 Device marking for TFBGA289 14 × 14 mm (B0NY)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 40. TFBGA289 marking example (package top view)



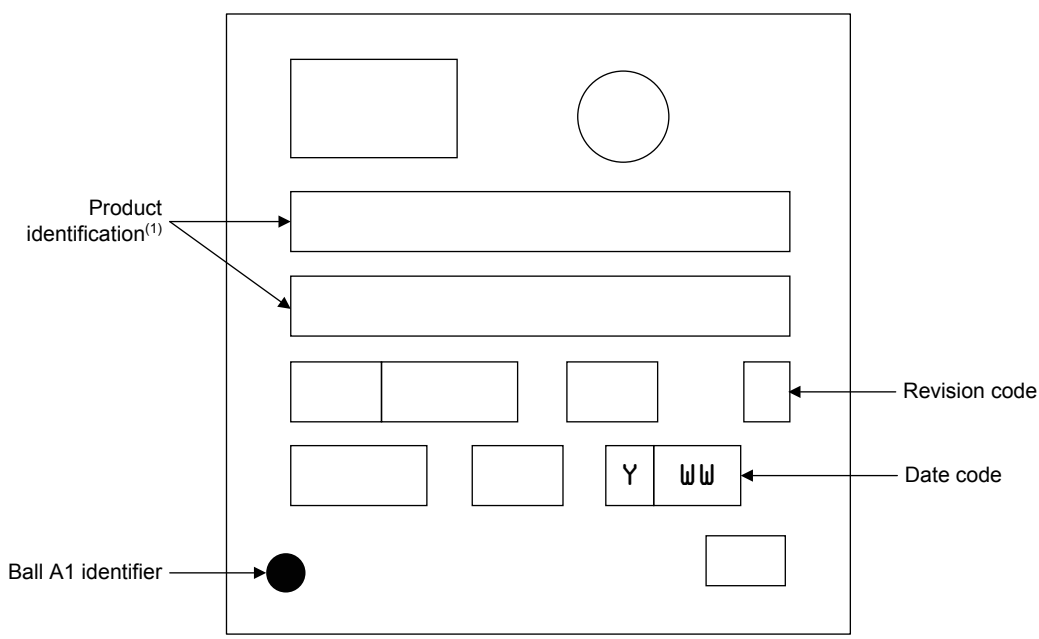
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST’s Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

8.10 Device marking for TFBGA320 11 × 11 mm (B0EC)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 41. TFBGA320 marking example (package top view)



B0EC_TFBGA320_DM_V1

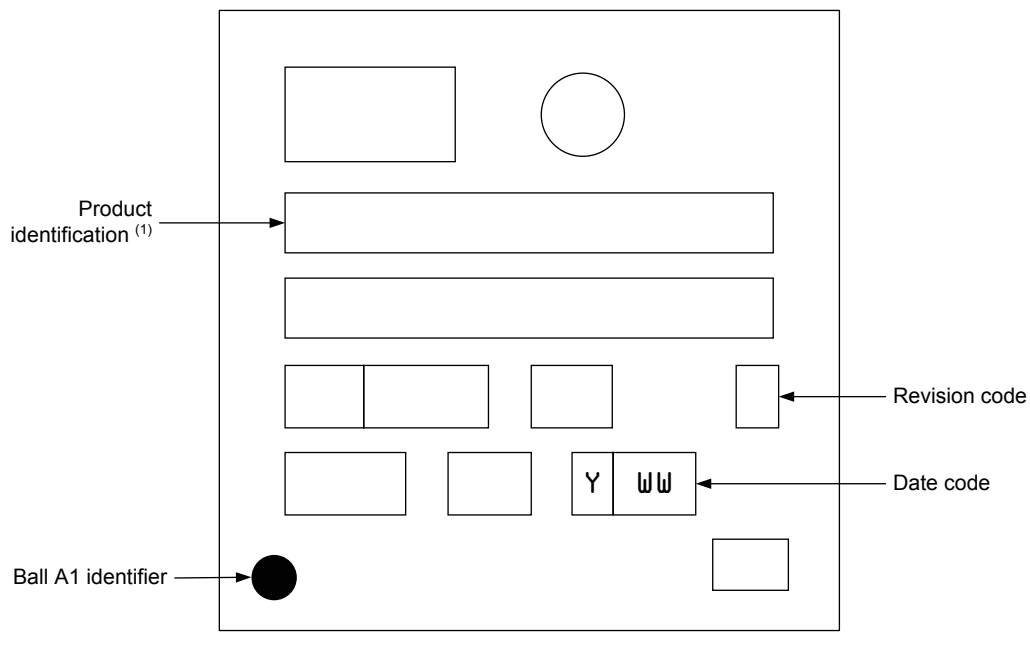
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST’s Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

8.11 Device marking for TFBGA361 12 × 12 mm (B031)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 42. TFBGA361 marking example (package top view)



B031_TFBGA361_DM_V1

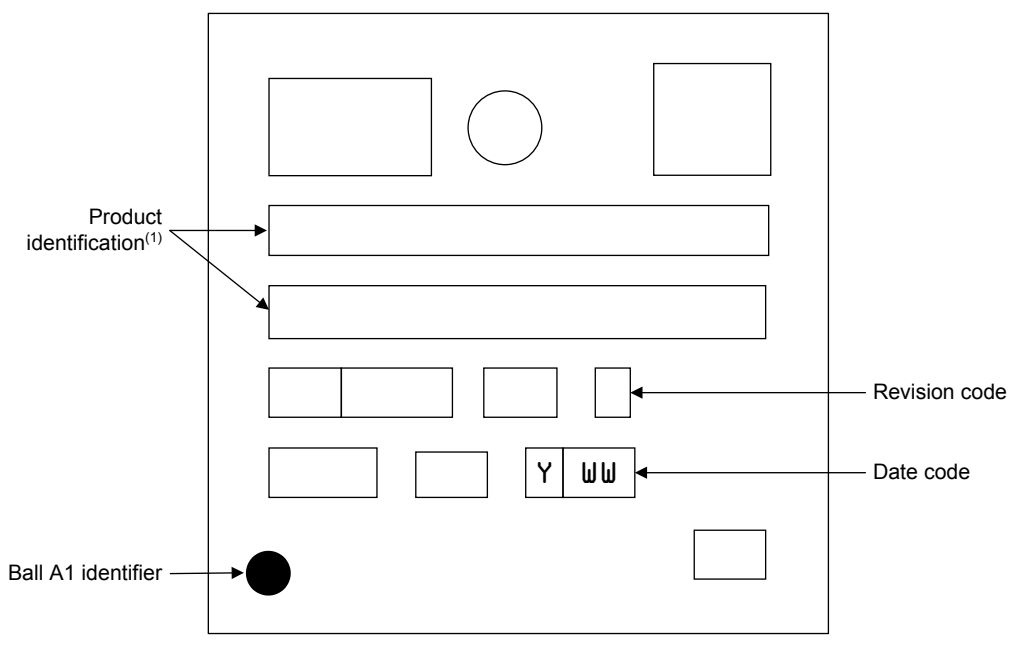
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST’s Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

8.12 Device marking for TFBGA361 16 × 16 mm (B0N8)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 43. TFBGA361 marking example (package top view)



B0N8_TFBGA361_DM_V1

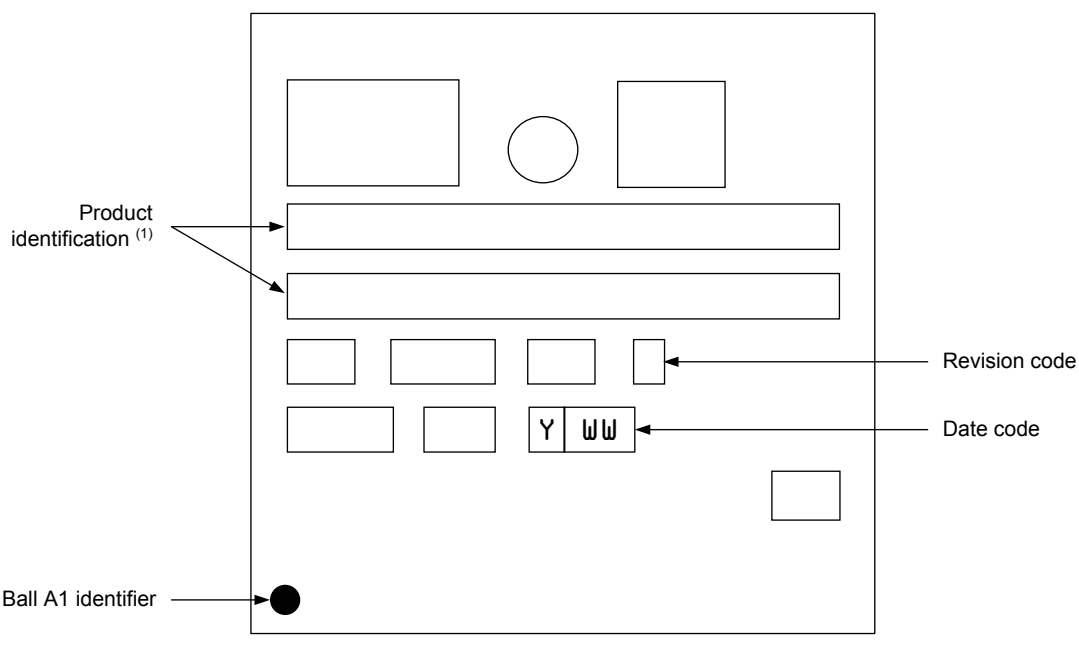
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

8.13 Device marking for TFBGA436 18 × 18 mm (B0D1)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 44. TFBGA436 marking example (package top view)



B0D1_TFBGA436_DM_V2

1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST’s Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

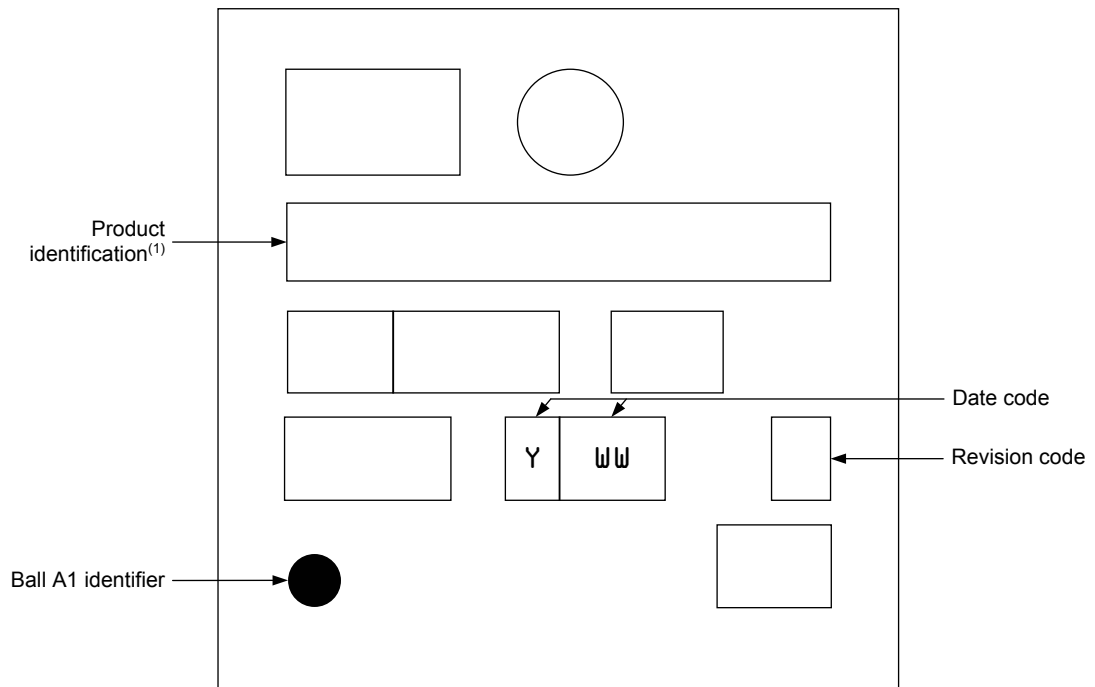
9 Device markings for UFBGA packages

9.1 Device marking for UFBGA59 5 × 5 mm (B0FS)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 45. UFBGA59 marking example (package top view)



B0FS_UFBGA59_DM_V3

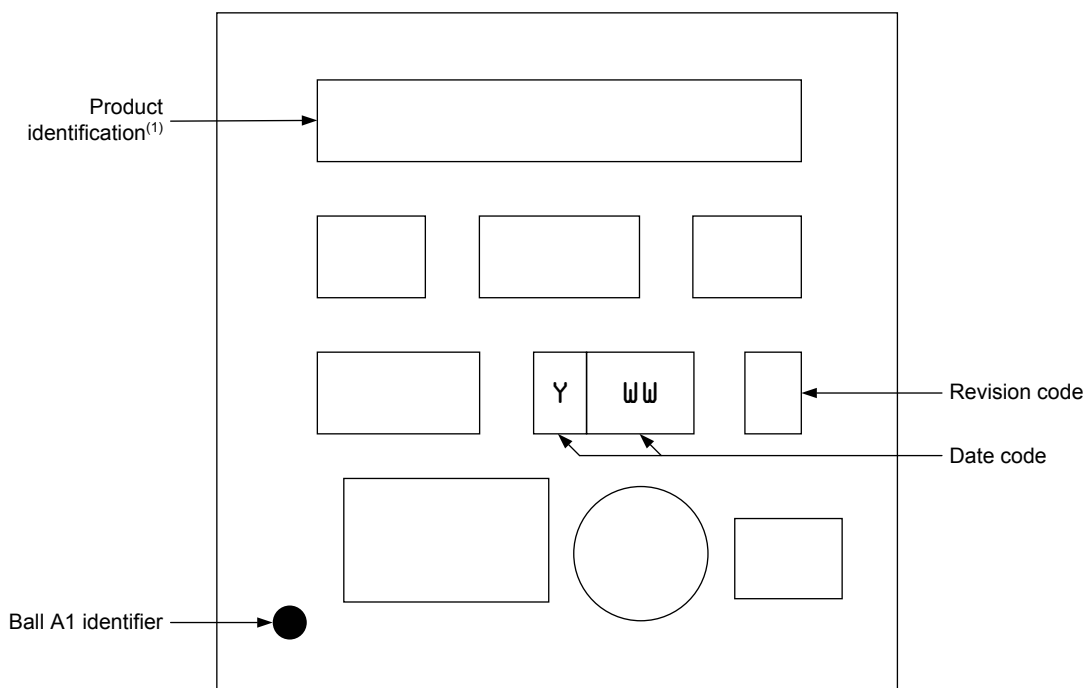
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST’s Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

9.2 Device marking for UFBGA64 5 × 5 mm (A019)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 46. UFBGA64 marking example (package top view)



A019_UFBGA64_DM_V3

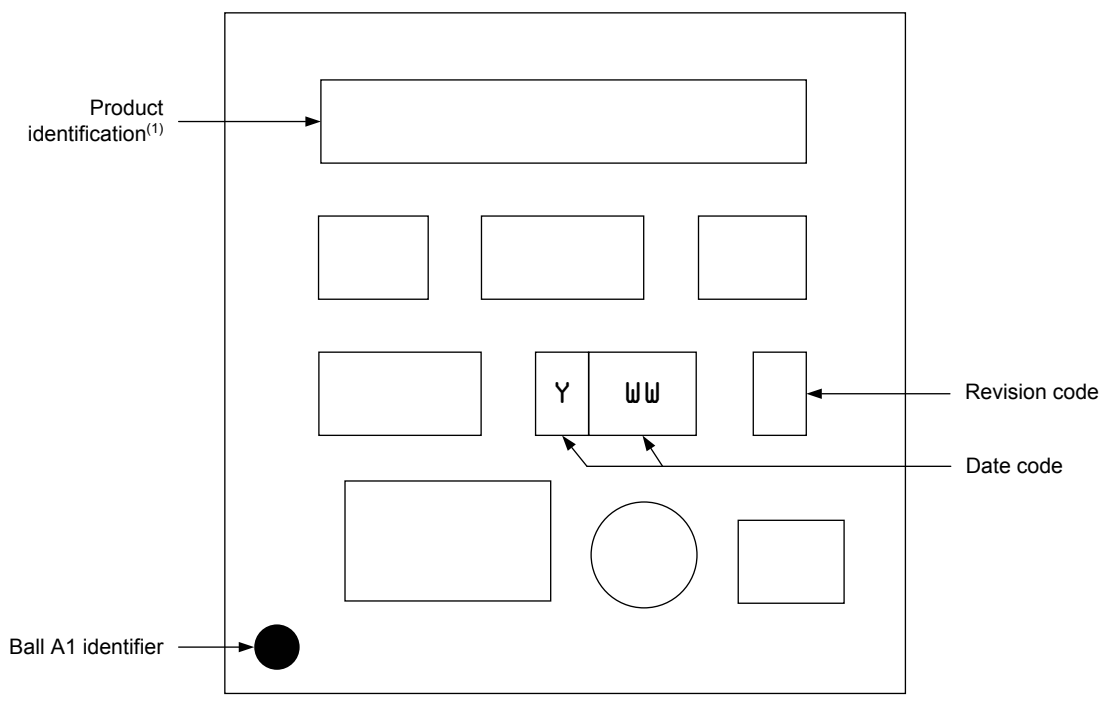
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST’s Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

9.3 Device marking for UFBGA73 5 × 5 mm (B08E)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 47. UFBGA73 marking example (package top view)



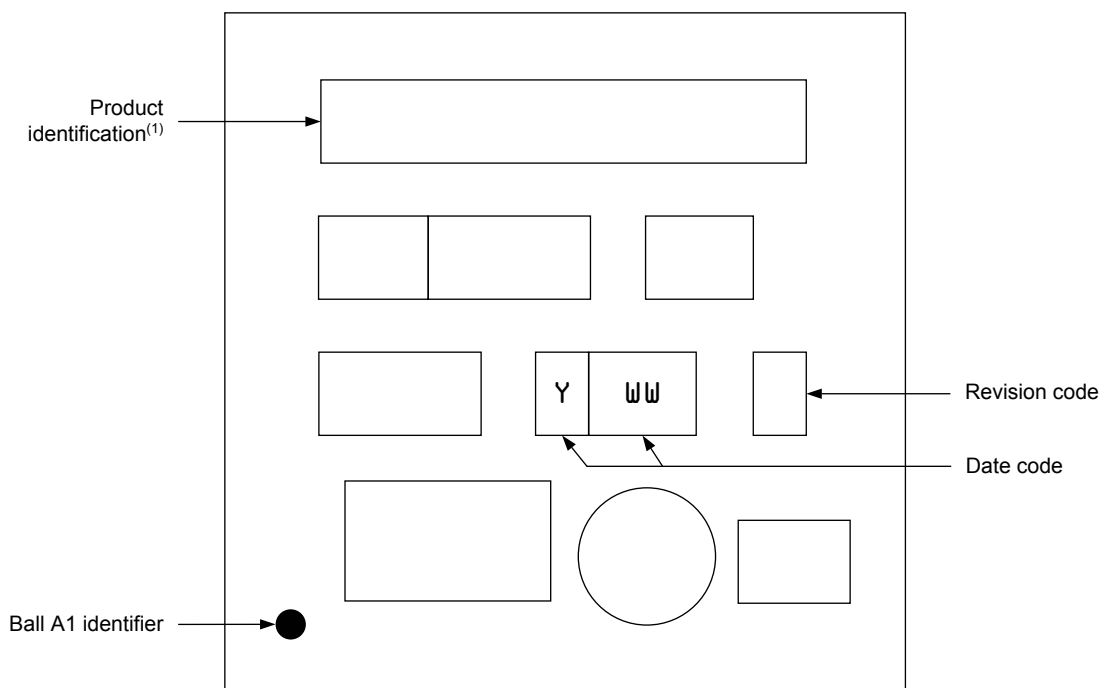
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST’s Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

9.4 Device marking for UFBGA81 5 × 5 mm (B0B8)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 48. UFBGA81 marking example (package top view)



B0B8_UFBGA81_DM_V1

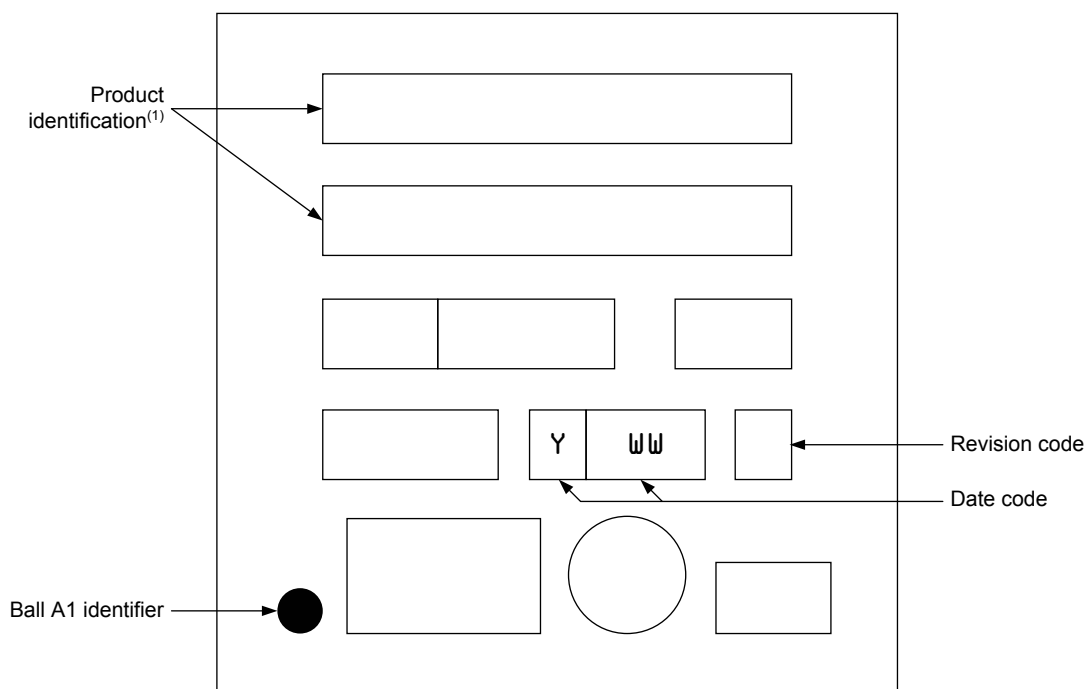
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST’s Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

9.5 Device marking for UFBGA100 7 × 7 mm (A0C2)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 49. UFBGA100 marking example (package top view)



A0C2_UFBGA100_DM_V3

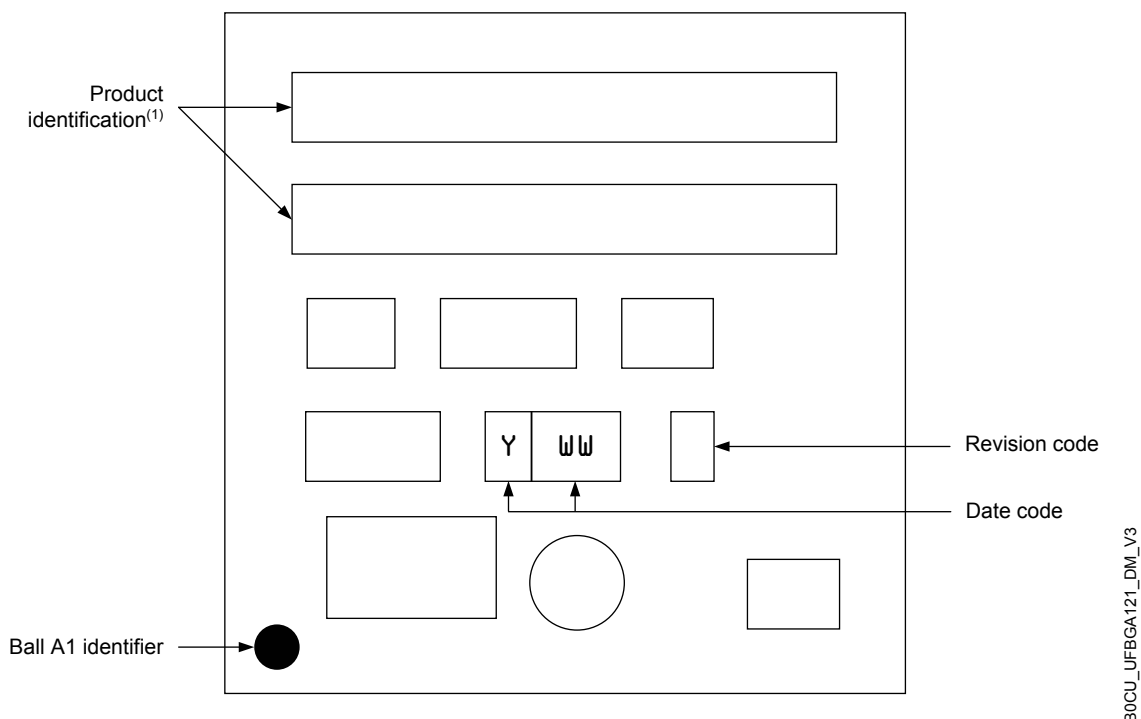
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

9.6 Device marking for UFBGA121 6 × 6 mm (B0CU)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 50. UFBGA121 marking example (package top view)



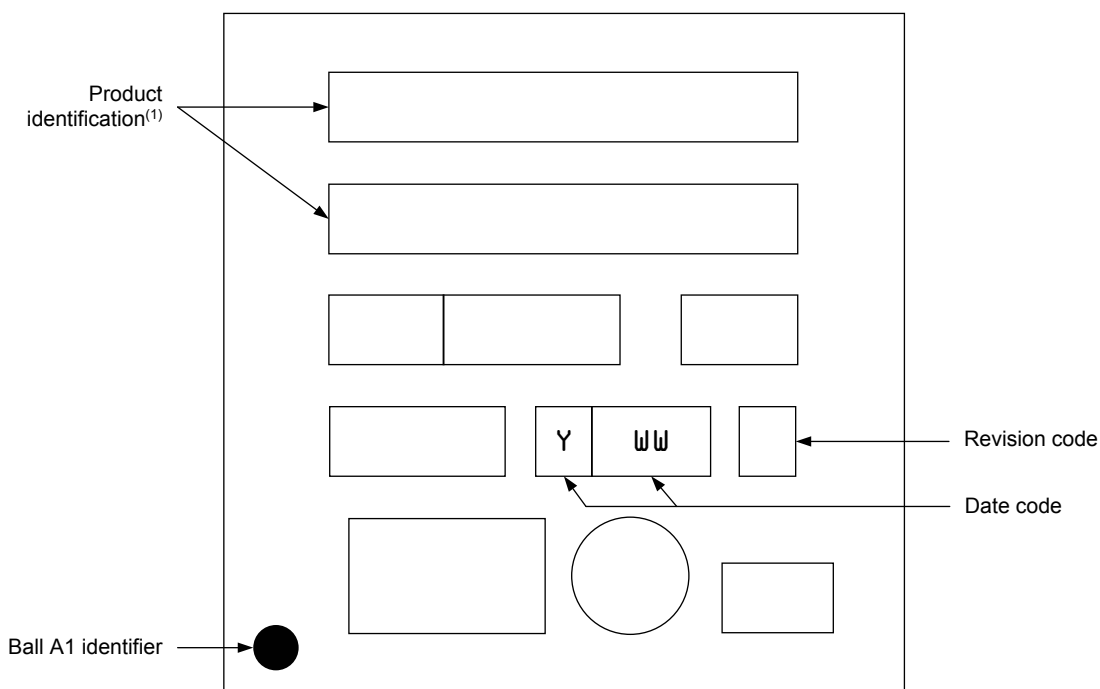
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST’s Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

9.7 Device marking for UFBGA129 7 × 7 mm (B09R)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 51. UFBGA129 marking example (package top view)



B09R_UFBGA129_DM_V4

1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

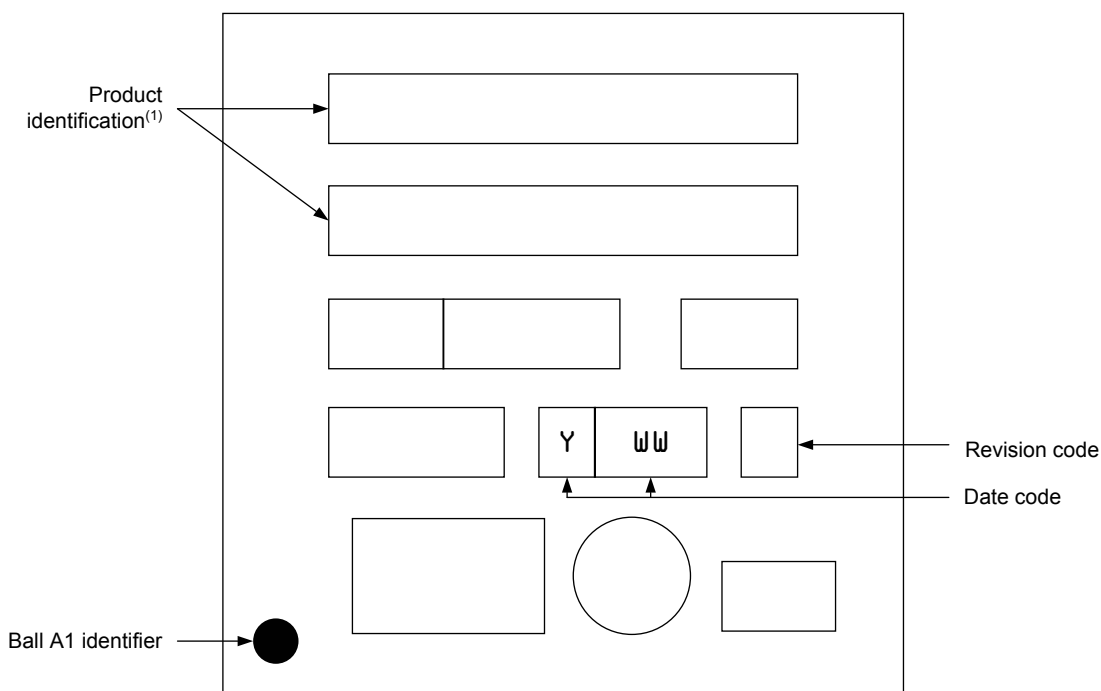
9.8

Device marking for UFBGA132 7 × 7 mm (A0G8)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 52. UFBGA132 marking example (package top view)



A0G8_UFBGA132_DM_V4

1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

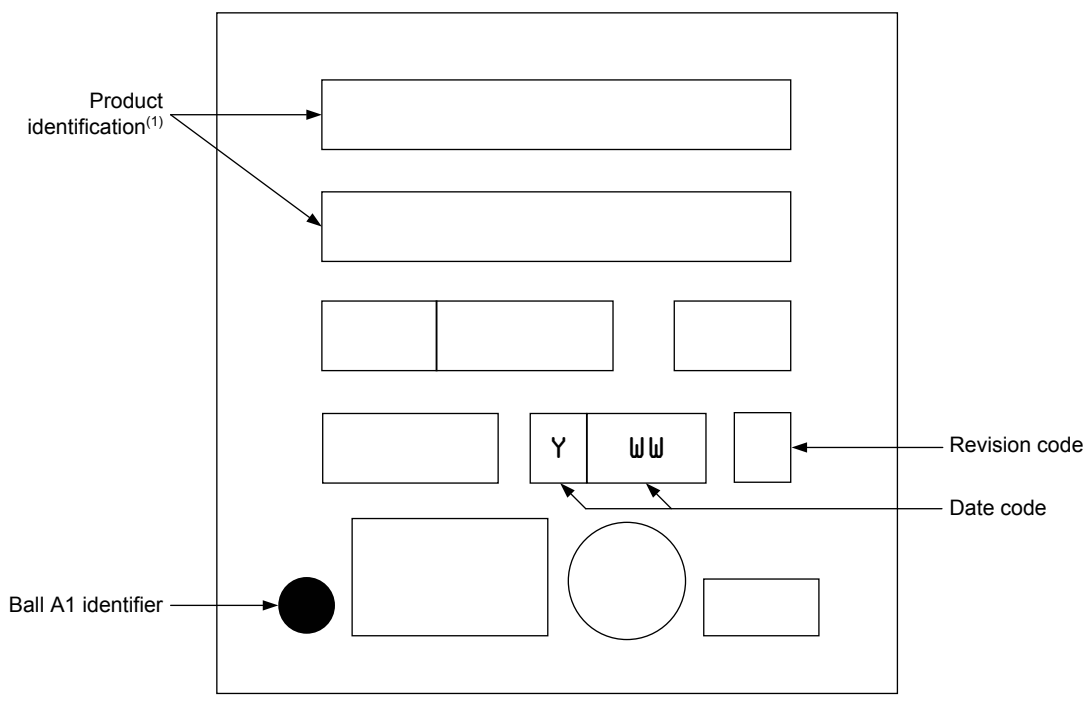
9.9

Device marking for UFBGA144 7 × 7 mm (A0AS)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 53. UFBGA144 marking example (package top view)



A0AS_UFBGA144_DM_V4

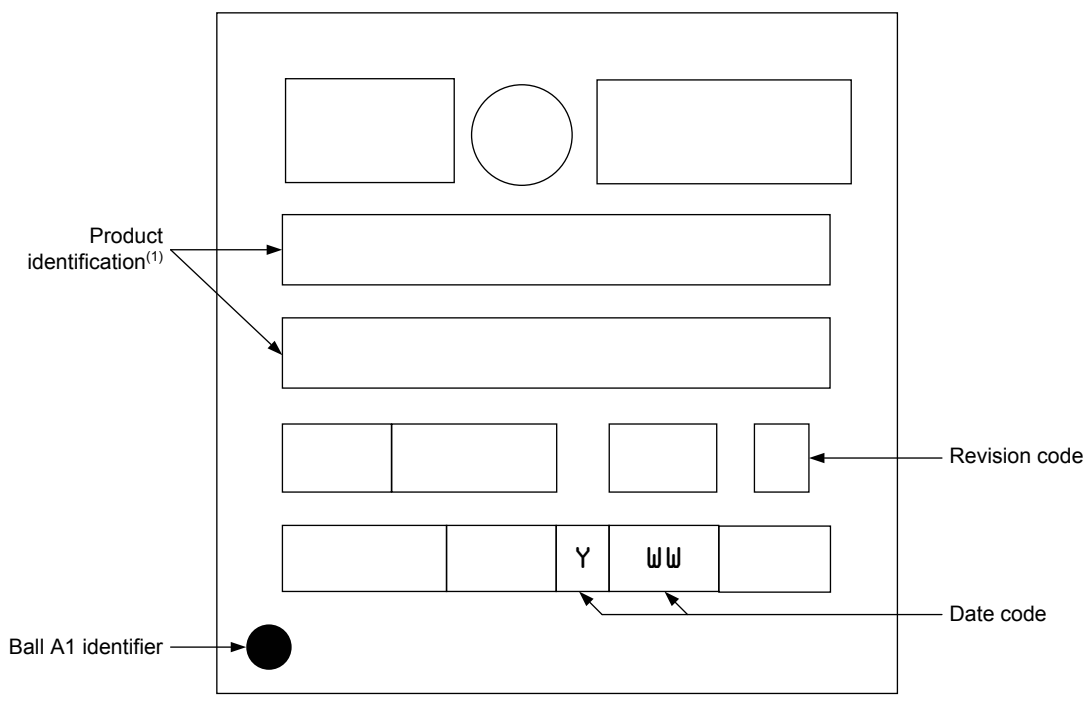
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

9.10 Device marking for UFBGA144 10 × 10 mm (A02Y)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 54. UFBGA144 marking example (package top view)



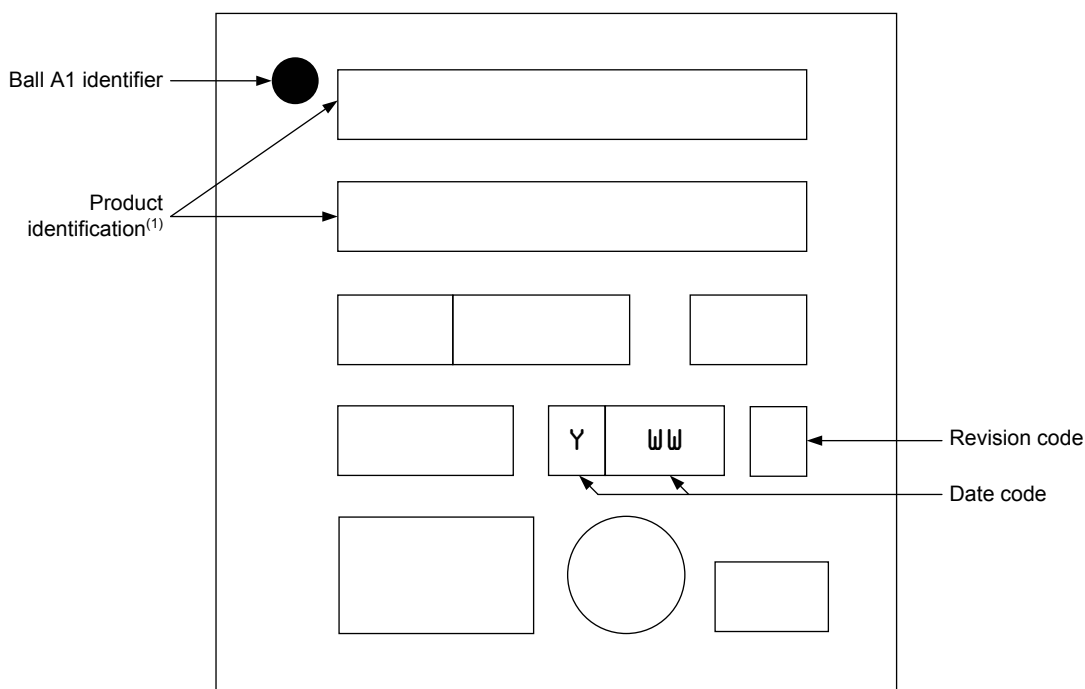
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

9.11 Device marking for UFBGA169 7 × 7 mm (A0YV)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at top left.

Figure 55. UFBGA169 marking example (package top view)



A0YV_UFBGA169_DM_V5

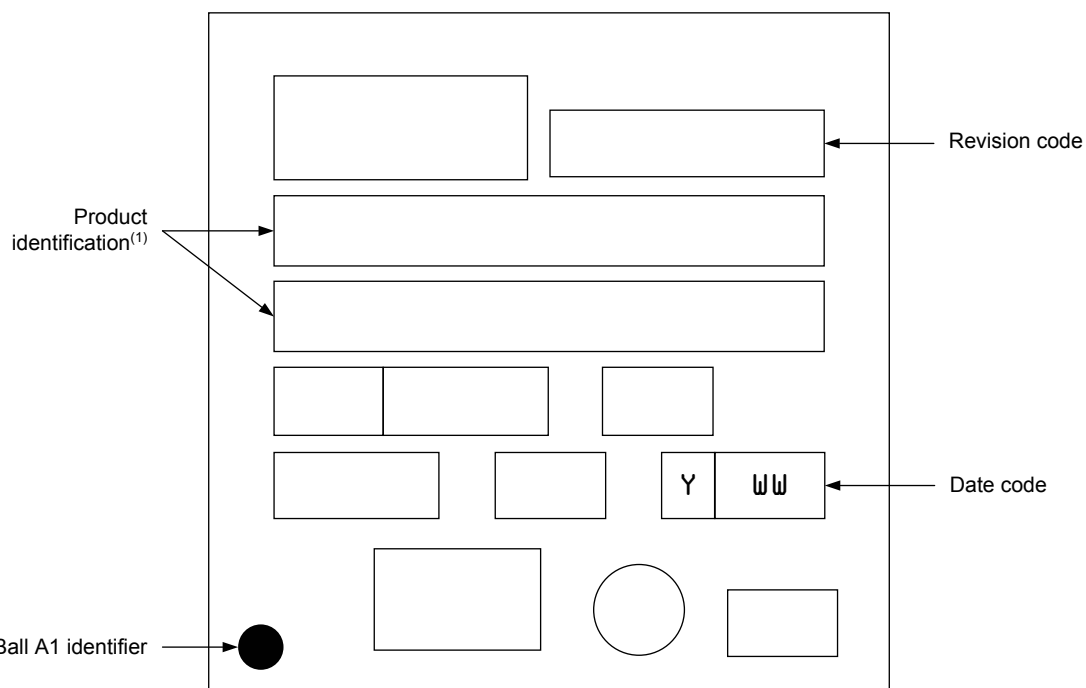
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

9.12 Device marking for UFBGA176+25 10 × 10 mm (A0E7)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 56. UFBGA176+25 marking example (package top view)



A0E7_UFBGA176+25_DM_V4

1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

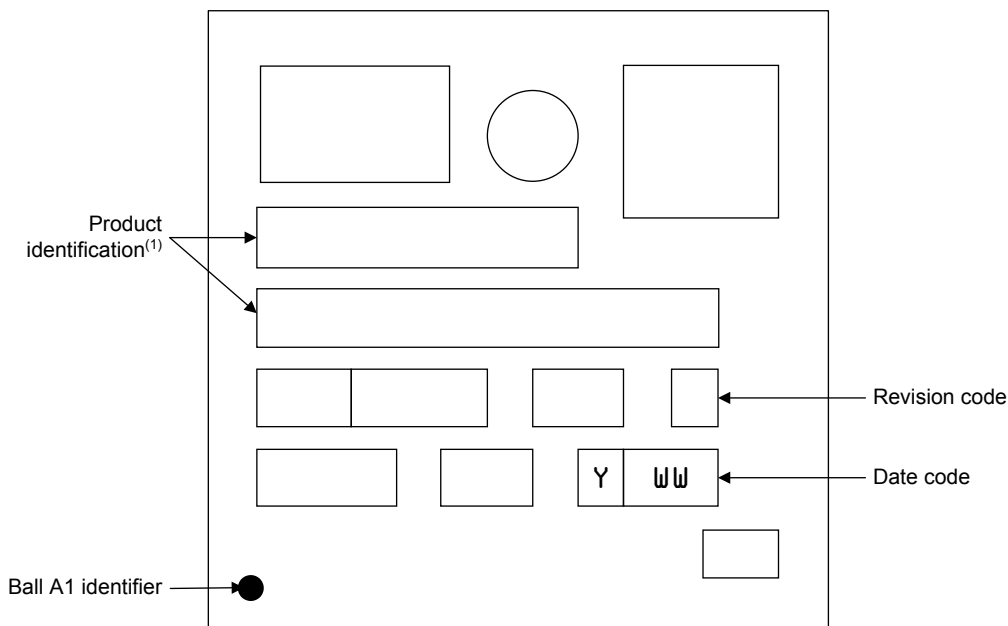
10 Device markings for VFBGA packages

10.1 Device marking for VFBGA142 8 × 8 mm (B0GM)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 57. VFBGA142 marking example (package top view)



B0GM_VFBGA142_DM_V1

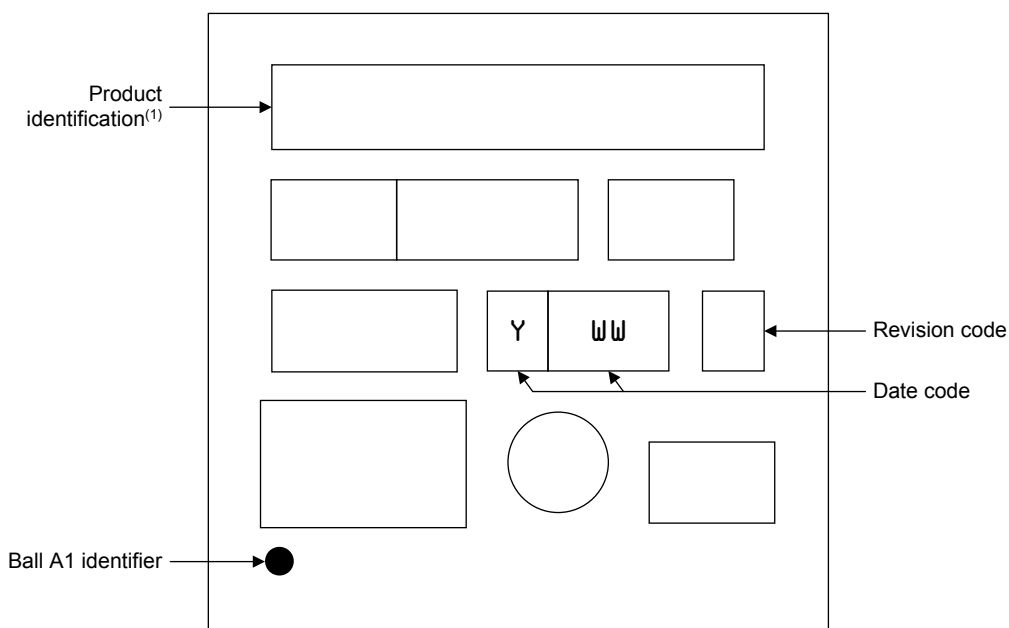
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

10.2 Device marking for VFBGA169 6 × 6 mm (B0LA)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 58. VFBGA169 marking example (package top view)



B0LA_VFBGA169_DM_V1

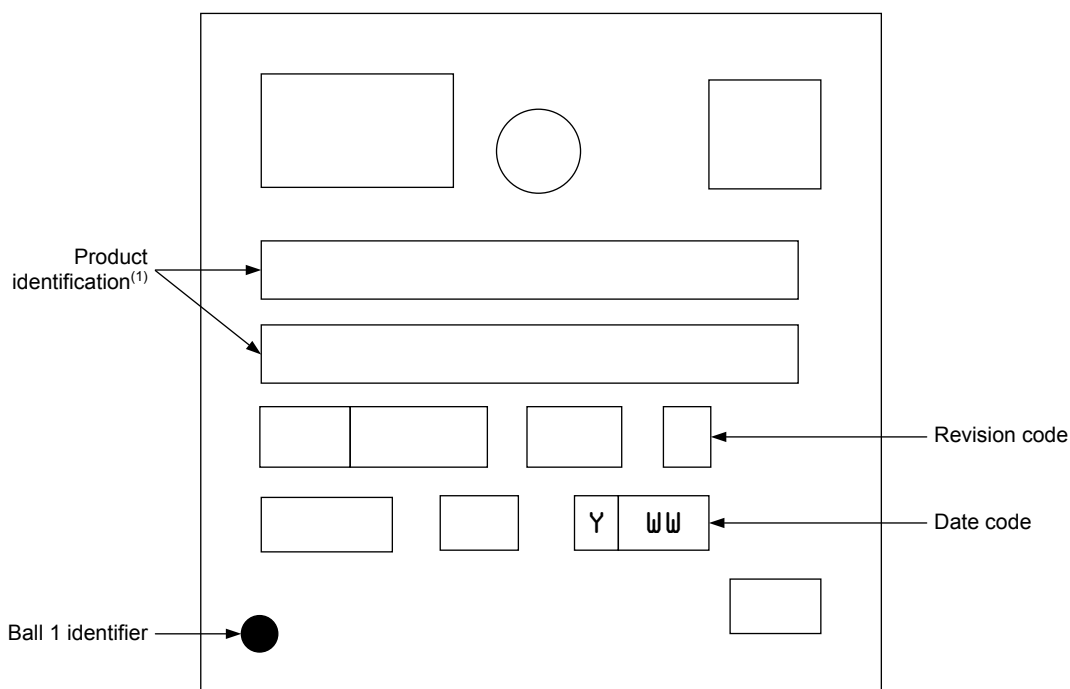
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

10.3 Device marking for VFBGA178 12 × 12 mm (B0GL)

The following figure gives an example of the locations and orientation of the marking areas versus ball 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball 1 is always located at bottom left.

Figure 59. VFBGA178 marking example (package top view)



B0GL_VFBGA178_DM_V1

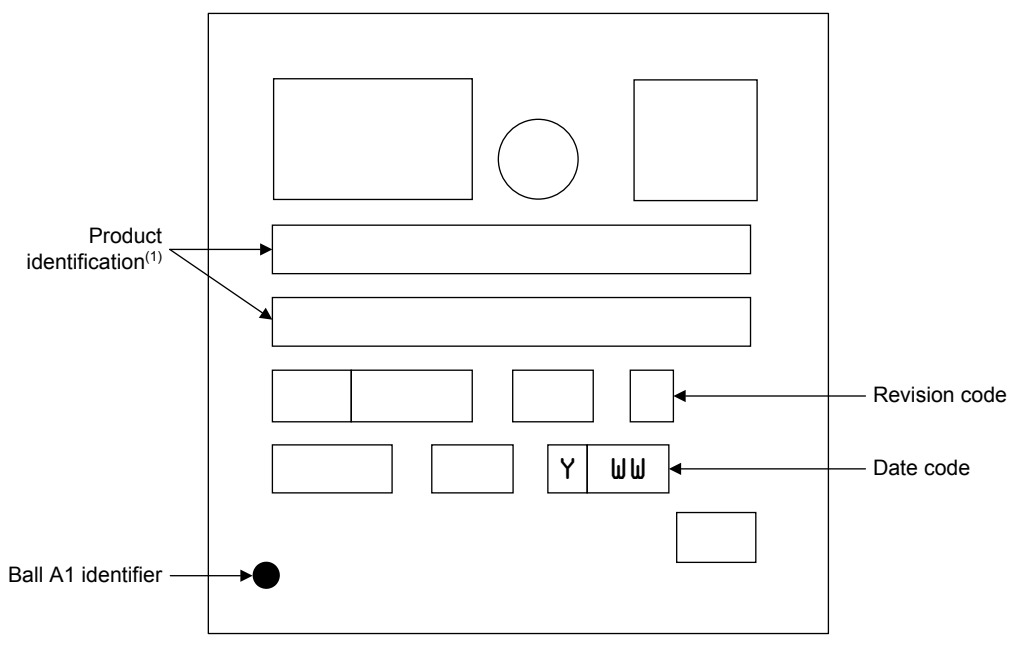
1. Parts marked as “ES”, “E” or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

10.4 Device marking for VFBGA198 10 × 10 mm (B0GJ)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 60. VFBGA198 marking example (package top view)



B0GJ_VFBGA198_DM_V1

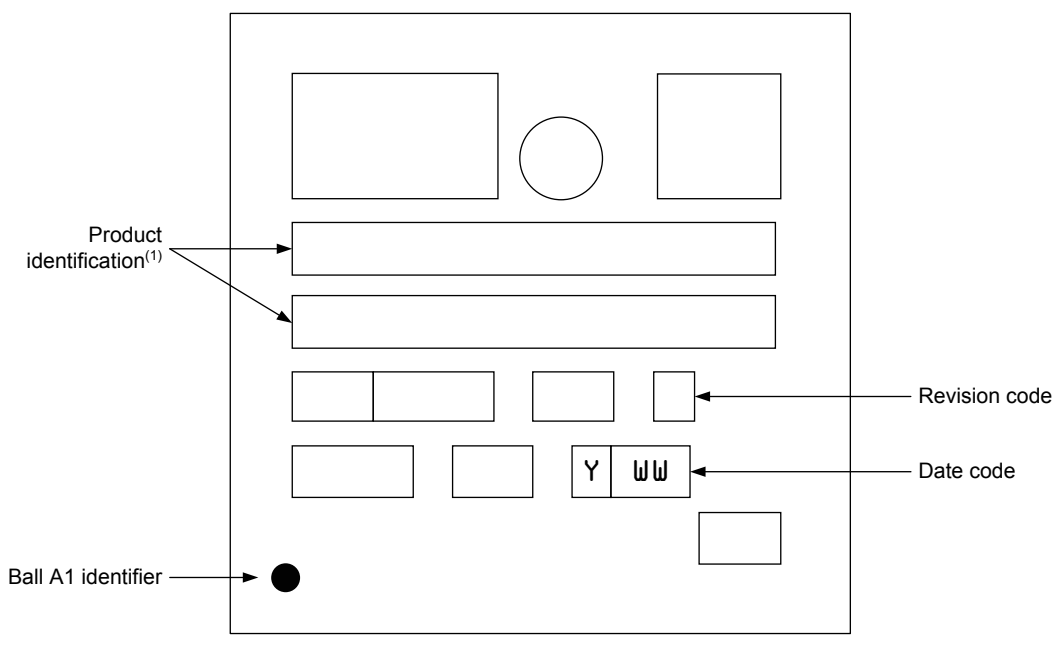
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10.5 Device marking for VFBGA223 10 × 10 mm (B0GK)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 61. VFBGA223 marking example (package top view)



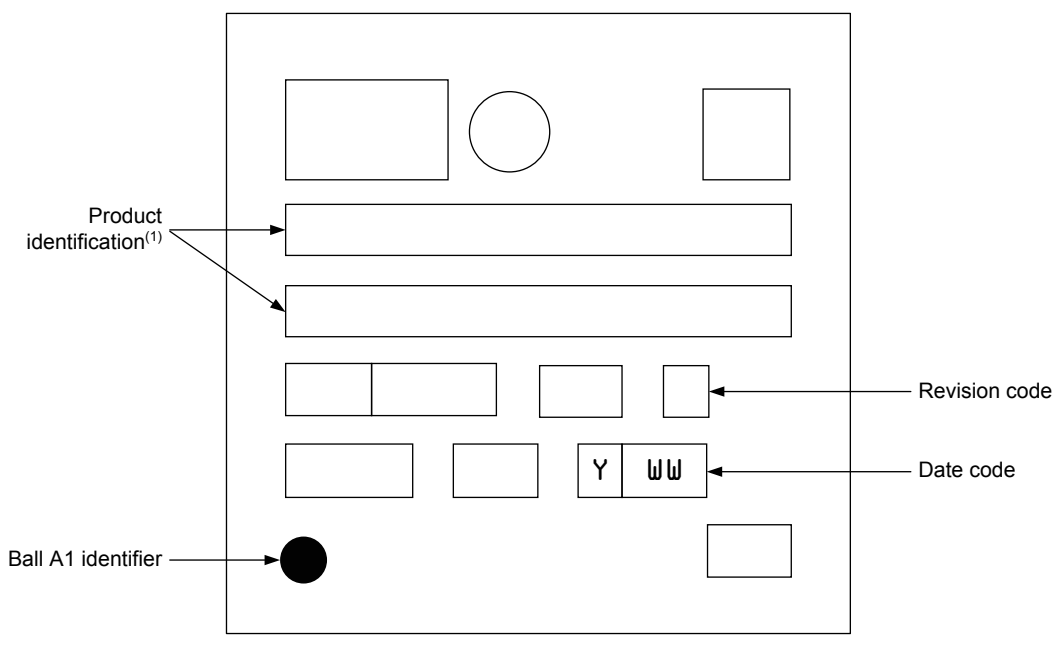
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10.6 Device marking for VFBGA264 14 × 14 mm (B0GH)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 62. VFBGA264 marking example (package top view)



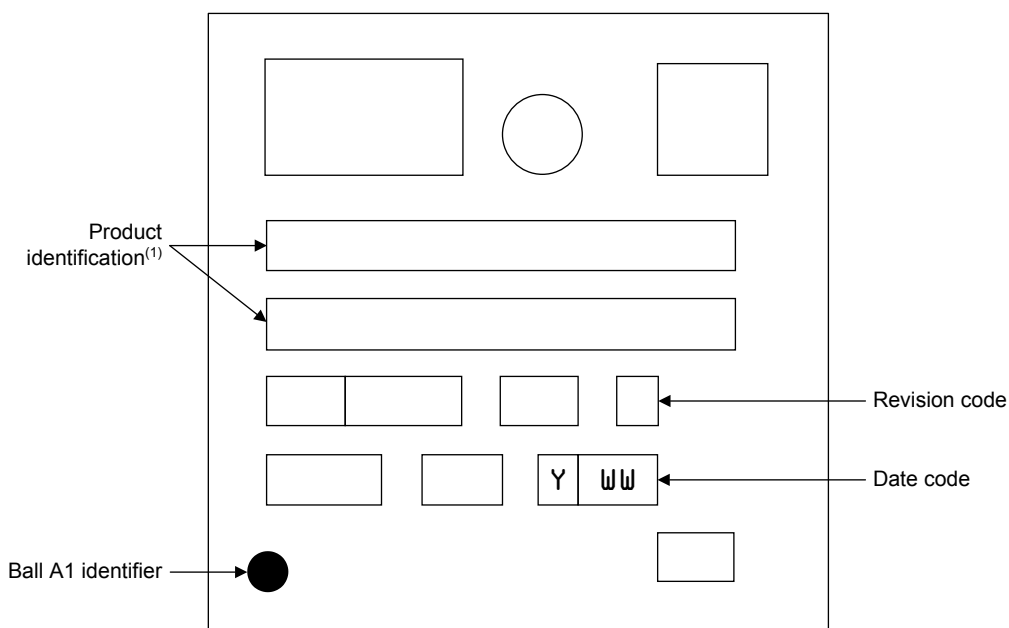
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10.7 Device marking for VFBGA273 11 × 11 mm (B0P7)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 63. VFBGA273 marking example (package top view)



B0P7_VFBGA273_DM_V2

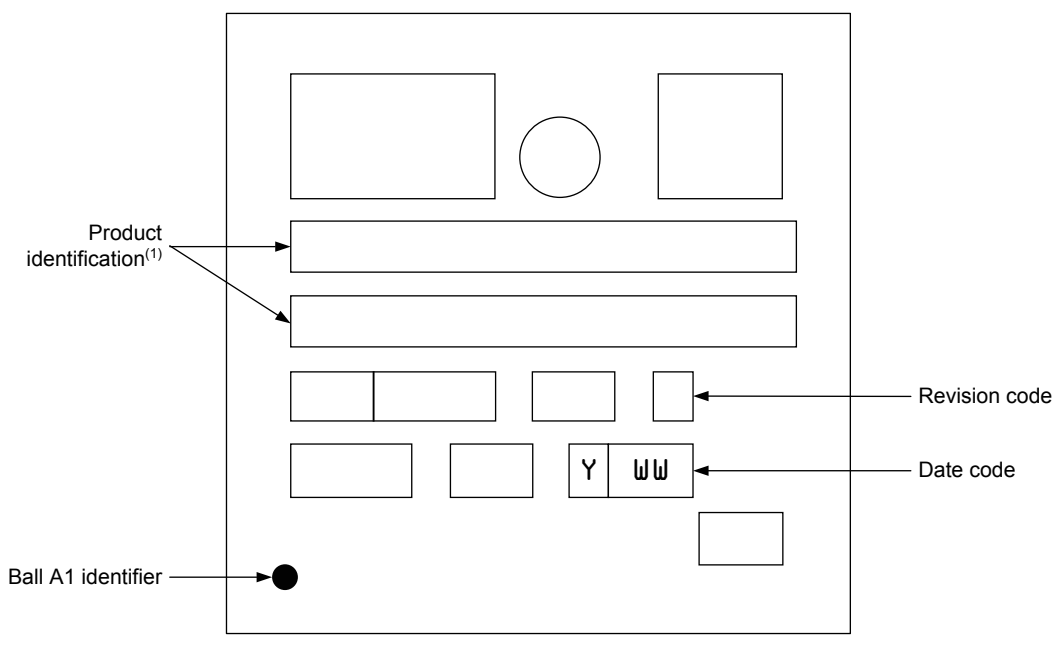
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10.8 Device marking for VFBGA361 10 × 10 mm (B09U)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 64. VFBGA361 marking example (package top view)



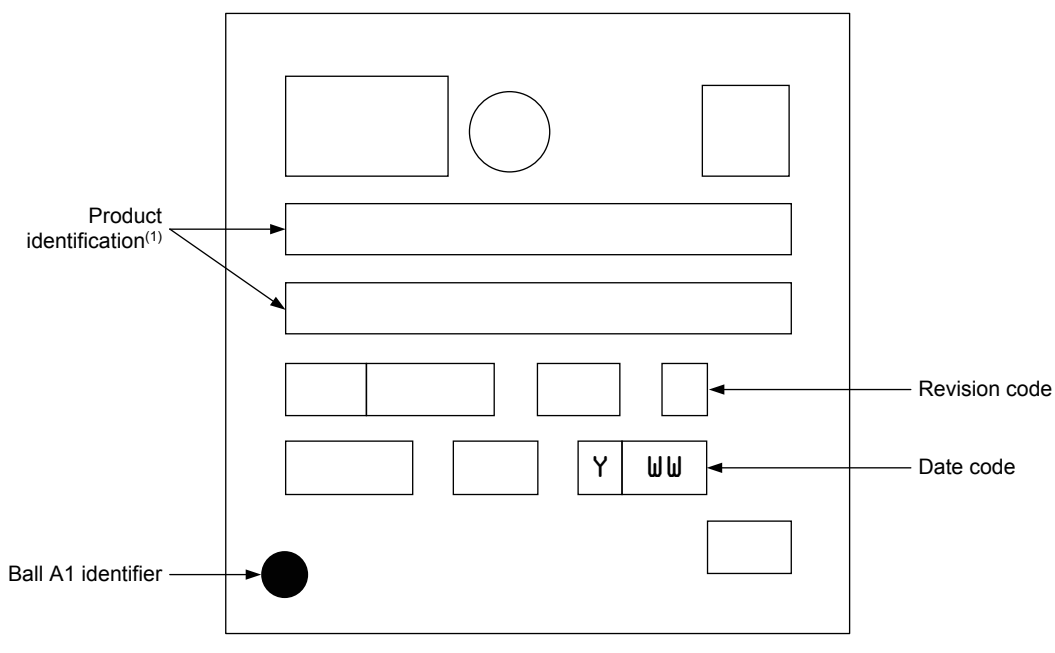
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10.9 Device marking for VFBGA424 14 × 14 mm (B0MP)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 65. VFBGA424 marking example (package top view)



B0MP_VFBGA424_DM_V1

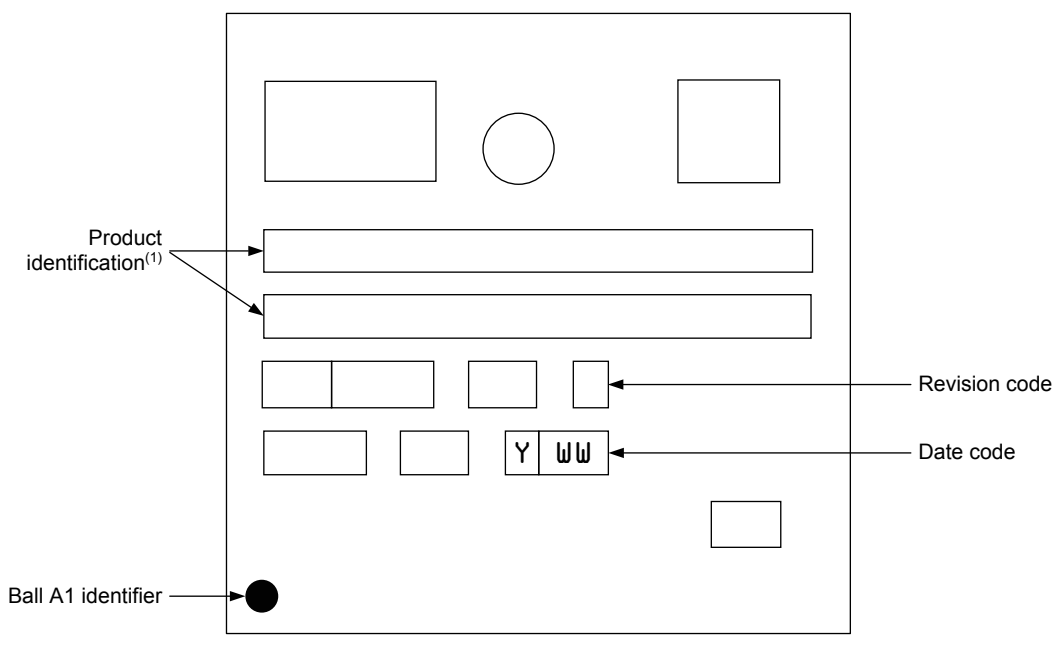
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10.10 Device marking for VFBGA436 18 × 18 mm (B0MS)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 66. VFBGA436 marking example (package top view)



B0MS_VFBGA436_DM_V1

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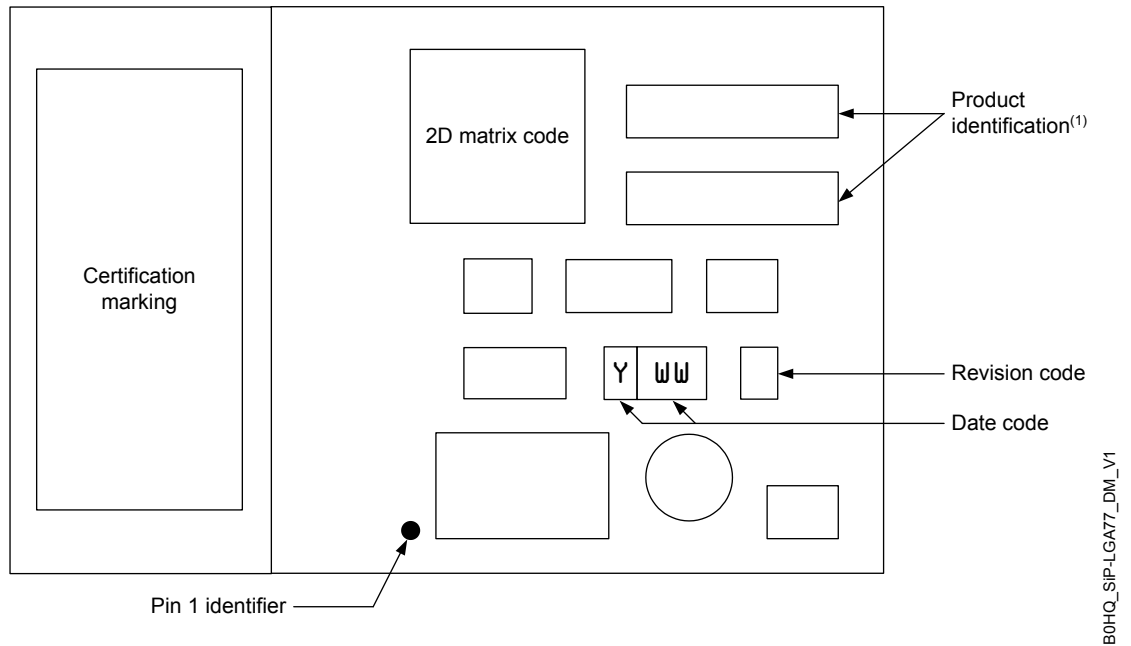
11 Device markings for SiP-LGA packages

11.1 Device marking for SiP-LGA77 6.5 × 10 mm (B0HQ)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 67. SiP-LGA77 marking example (package top view)



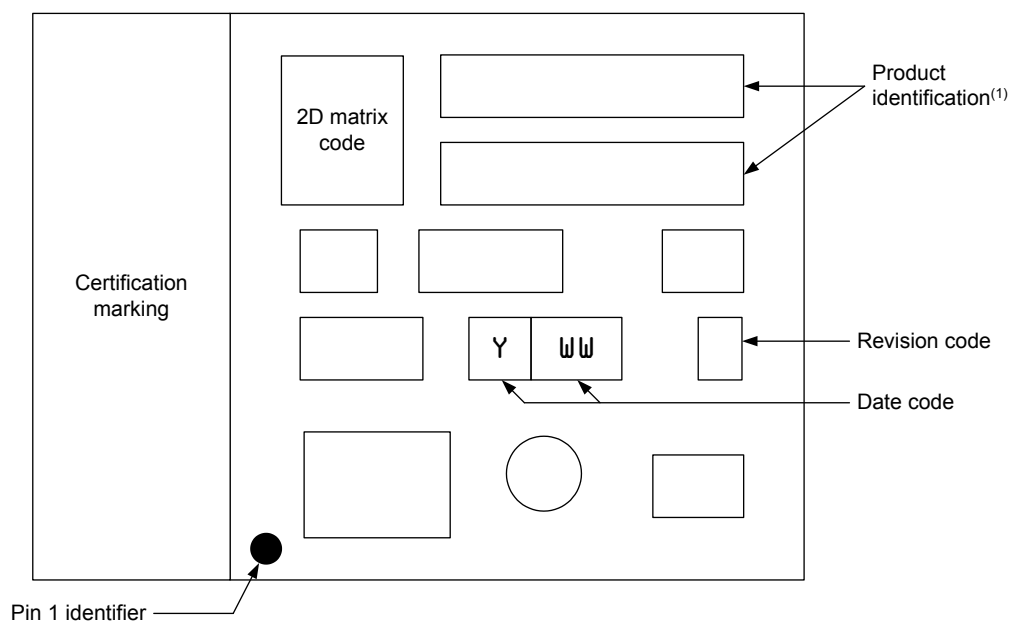
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11.2 Device marking for SiP-LGA86 7.3 × 11 mm (B0CQ)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 68. SiP-LGA86 marking example (package top view)



B0CQ_SiP-LGA86_DM_V1

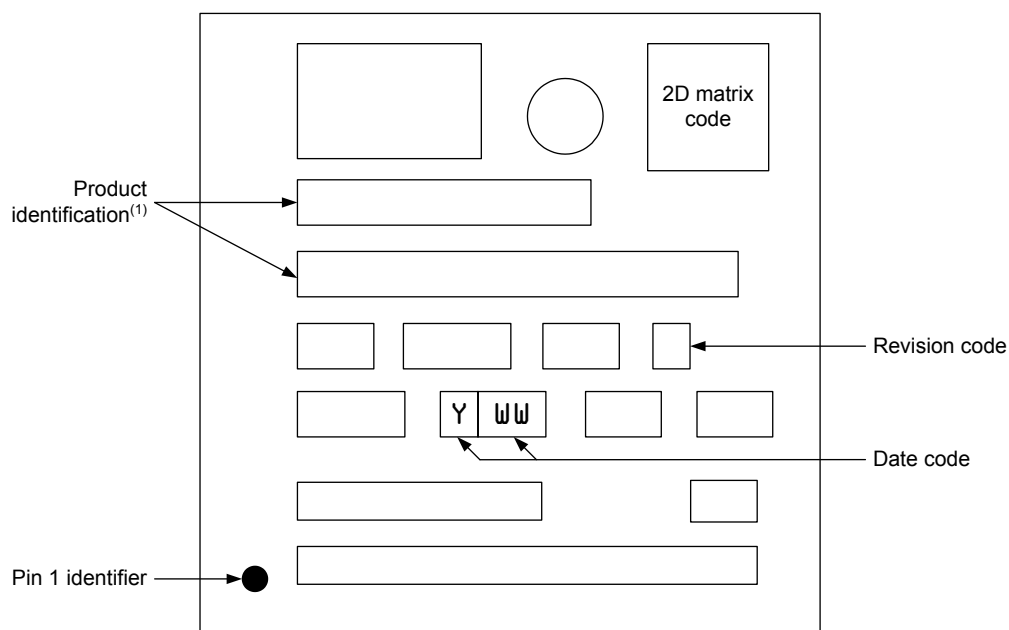
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11.3 Device marking for SiP-LGA92 10 × 10 mm (B0HB)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 69. SiP-LGA92 marking example (package top view)



B0HB_SiP-LGA92_DM_V1

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Revision history

Table 1. Document revision history

Date	Version	Changes
13-Jan-2023	1	Initial release.
29-Jun-2023	2	The whole document has been updated with the exception of the figures that kept the same index (same vertical text in bottom right corner) as in Rev 1 of the document.
19-Sep-2023	3	<p>Updated Section Introduction .</p> <p>Grouped similar package types into corresponding level 1 sections.</p> <p>Added following device marking sections:</p> <ul style="list-style-type: none"> • Section 5.5: Device marking for UFQFPN48 6 × 6 mm (A0F2) • Section 6.3: Device marking for VFQFPN48 6 × 6 mm (A0BE) • Section 9.4: Device marking for UFBGA81 5 × 5 mm (B0B8) • Section 10.3: Device marking for VFBGA178 12 × 12 mm (B0GL) • Section 10.5: Device marking for VFBGA223 10 × 10 mm (B0GK) • Section 10.6: Device marking for VFBGA264 14 × 14 mm (B0GH) • Section 11.1: Device marking for SiP-LGA77 6.5 × 10 mm (B0HQ)
22-Apr-2024	4	<p>Added following device marking sections:</p> <ul style="list-style-type: none"> • Section 10.8: Device marking for VFBGA361 10 × 10 mm (B09U) • Section 10.9: Device marking for VFBGA424 14 × 14 mm (B0MP) • Section 10.10: Device marking for VFBGA436 18 × 18 mm (B0MS)
16-Jul-2024	5	<p>Added following device marking sections:</p> <ul style="list-style-type: none"> • Section 8.9: Device marking for TFBGA289 14 × 14 mm (B0NY) • Section 8.12: Device marking for TFBGA361 16 × 16 mm (B0N8) • Section 10.2: Device marking for VFBGA169 6 × 6 mm (B0LA) • Section 10.4: Device marking for VFBGA198 10 × 10 mm (B0GJ) • Section 10.7: Device marking for VFBGA273 11 × 11 mm (B0P7)

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